

Amendments to the Drawings:

The attached replacement drawing sheets makes changes to Figs. 1-48 and replace the original sheets with Figs. 1-48.

Approved
1-30-04
mw

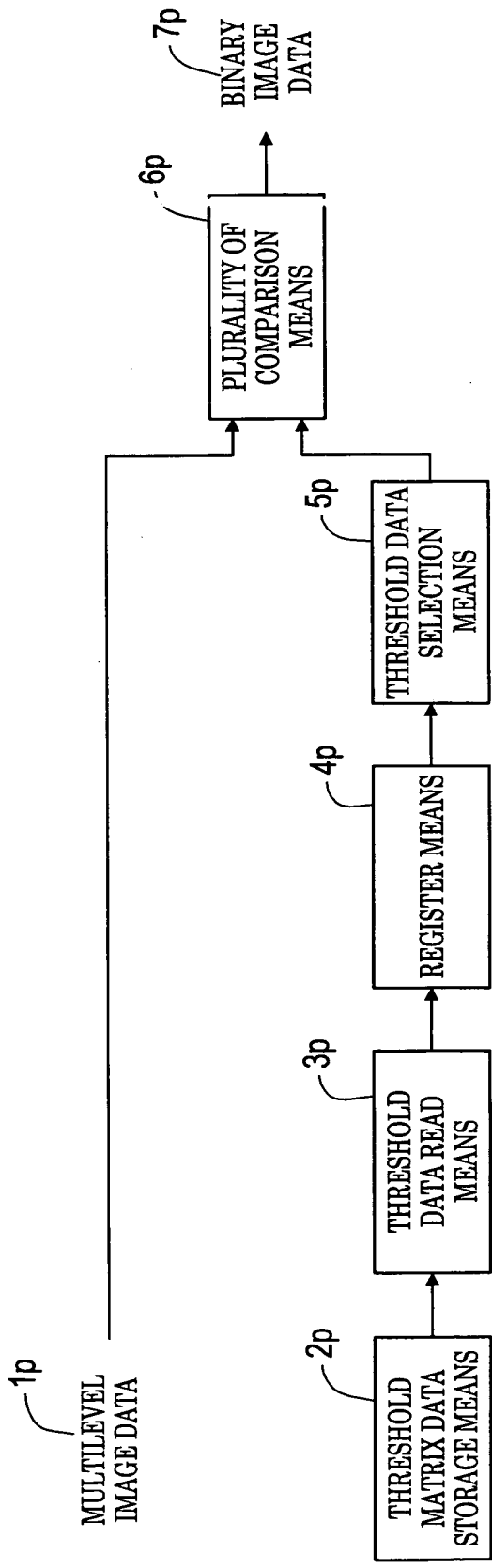
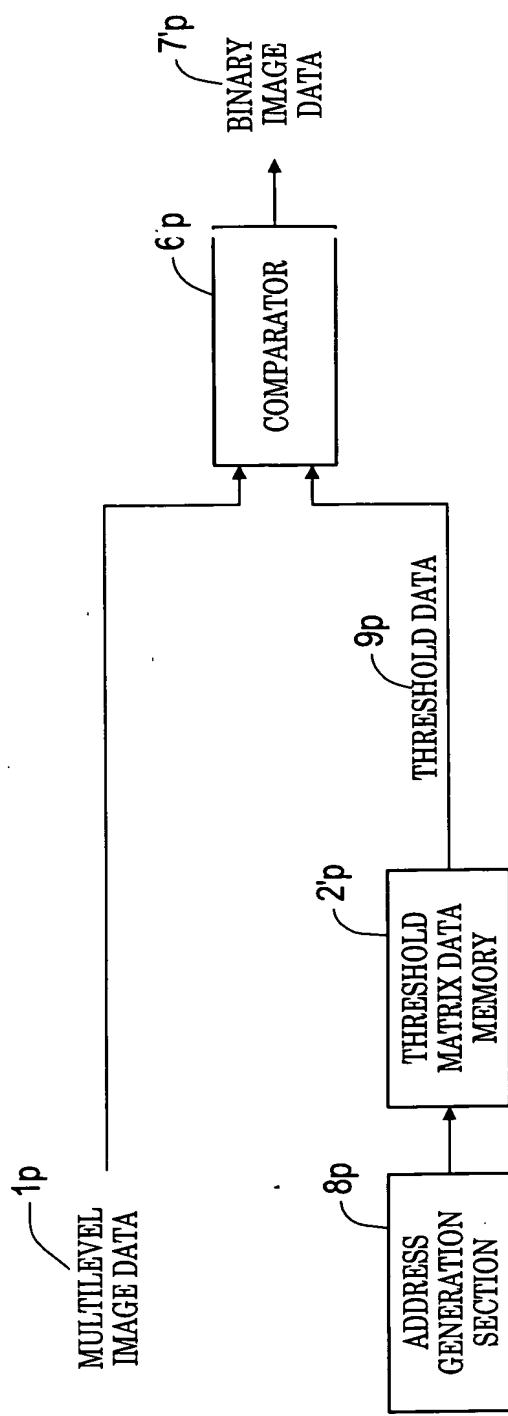


Fig. 1



"PRIOR ART" (mu) see amendment 8/25/03

Fig. 2

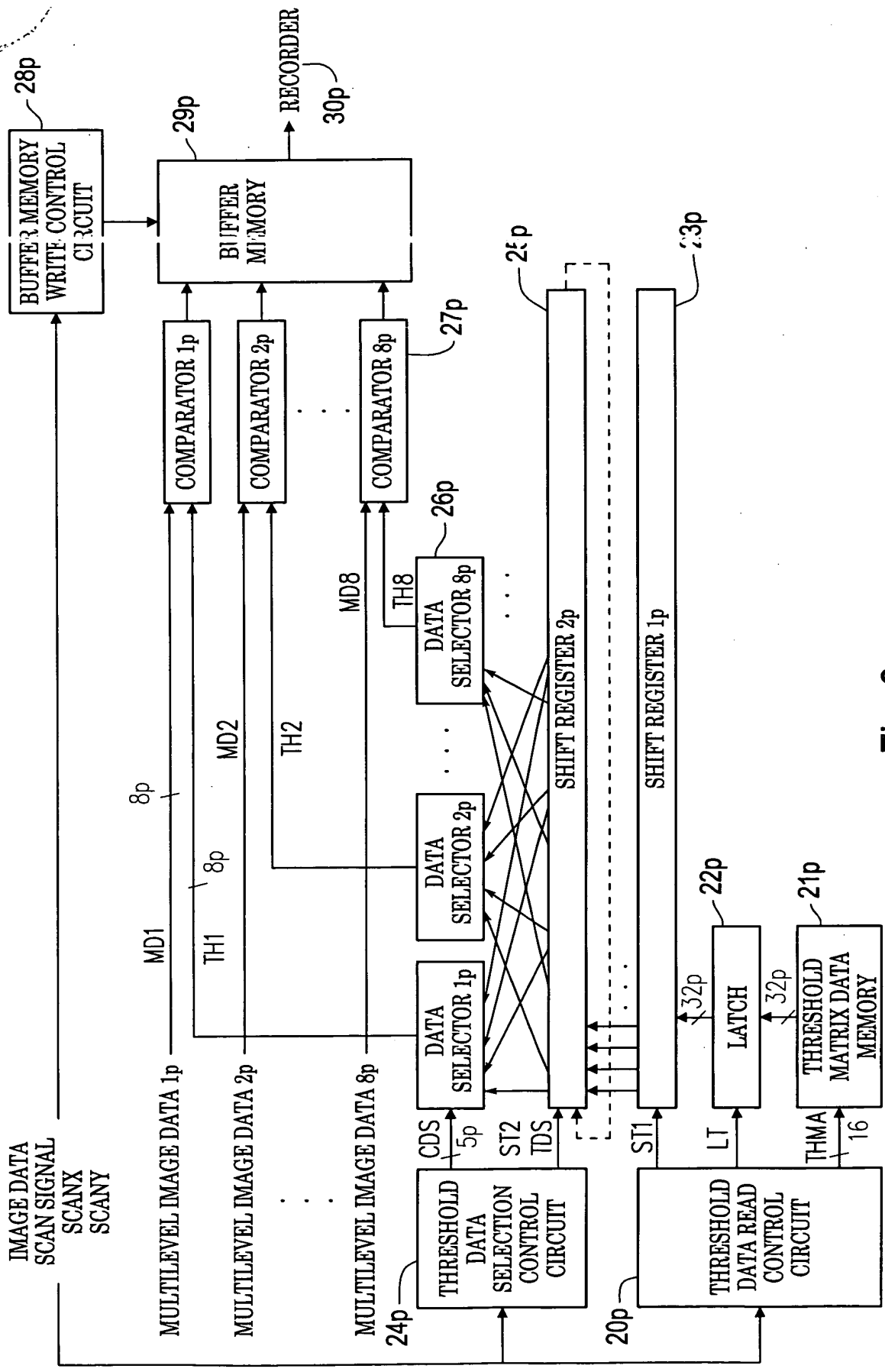


Fig. 3

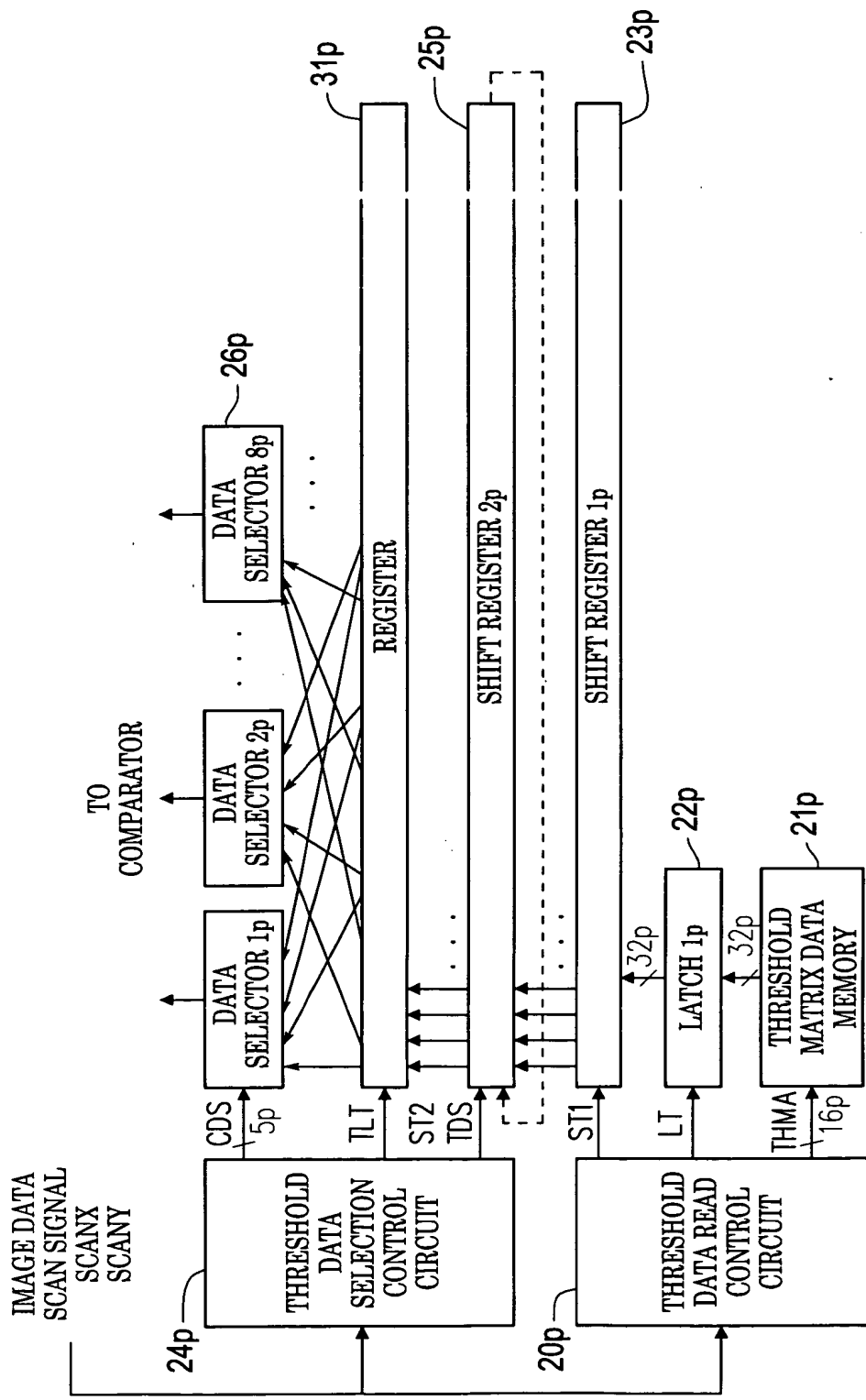


Fig. 4

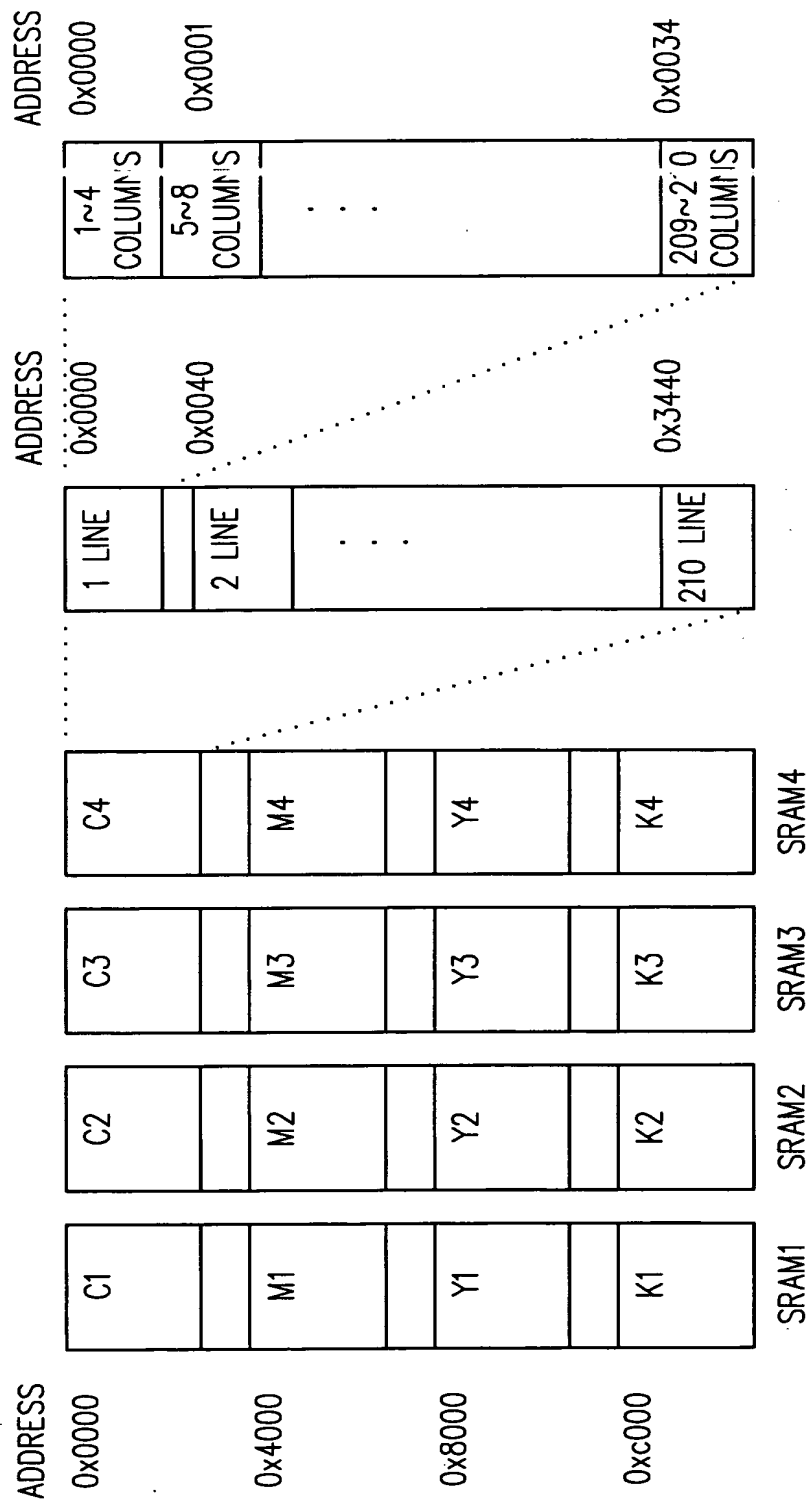


Fig. 5

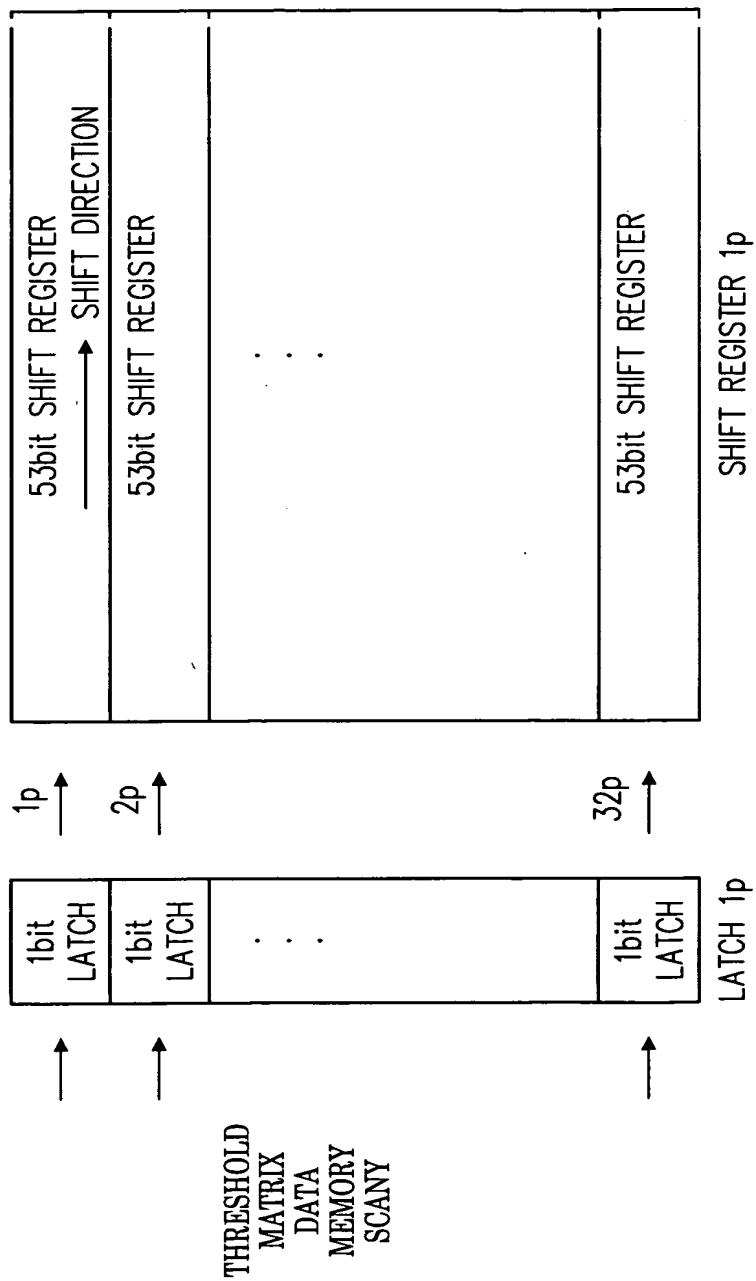


Fig. 6

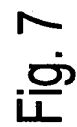


Fig. 7

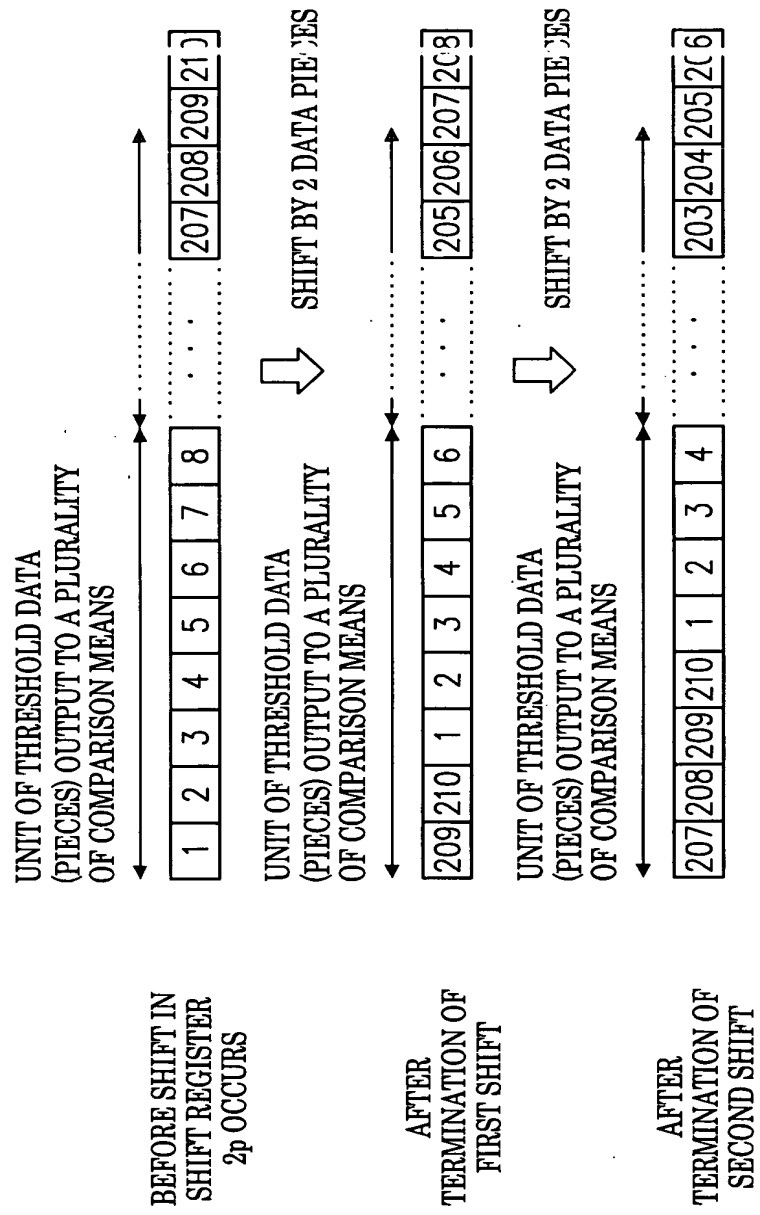


Fig. 8

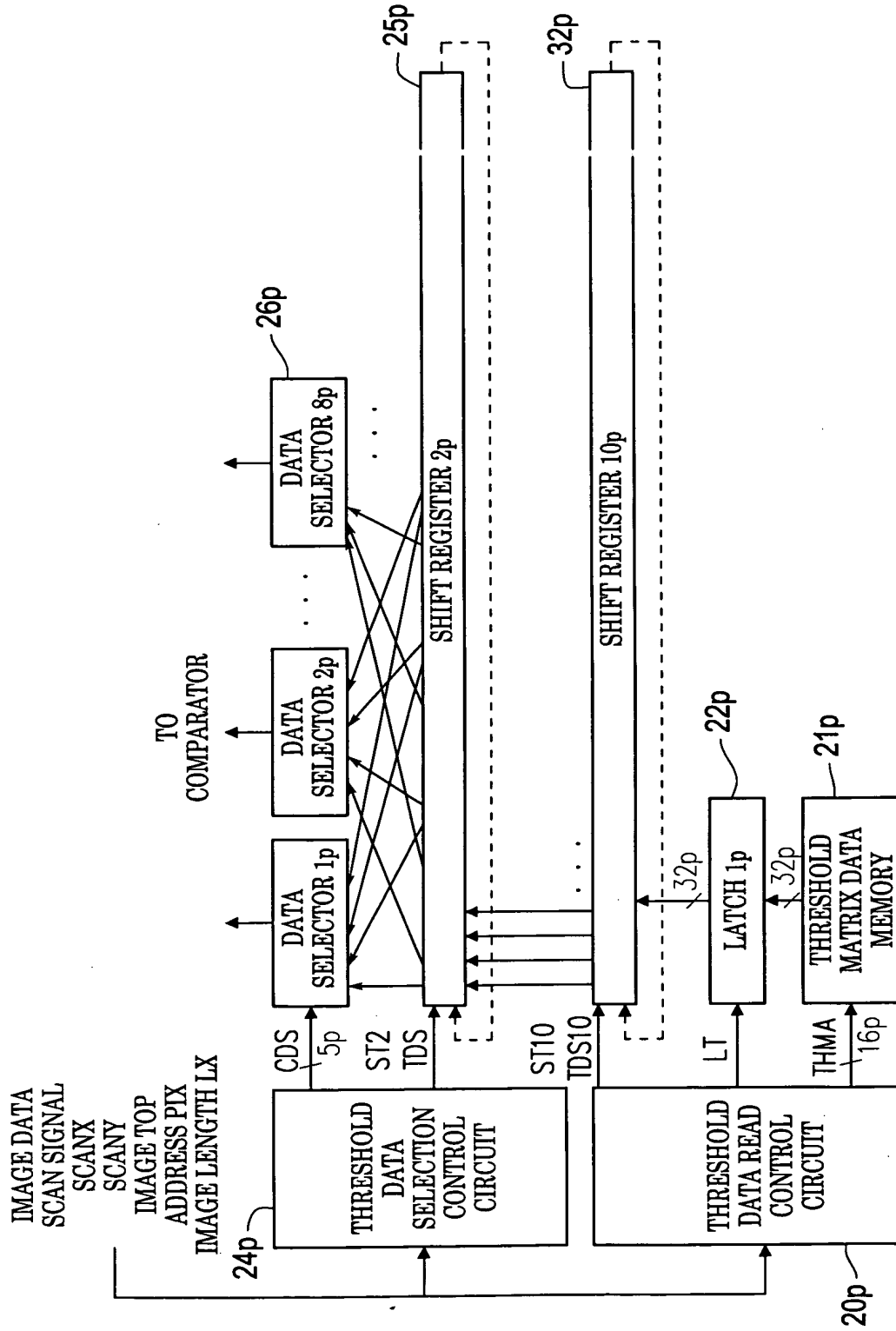


Fig. 9

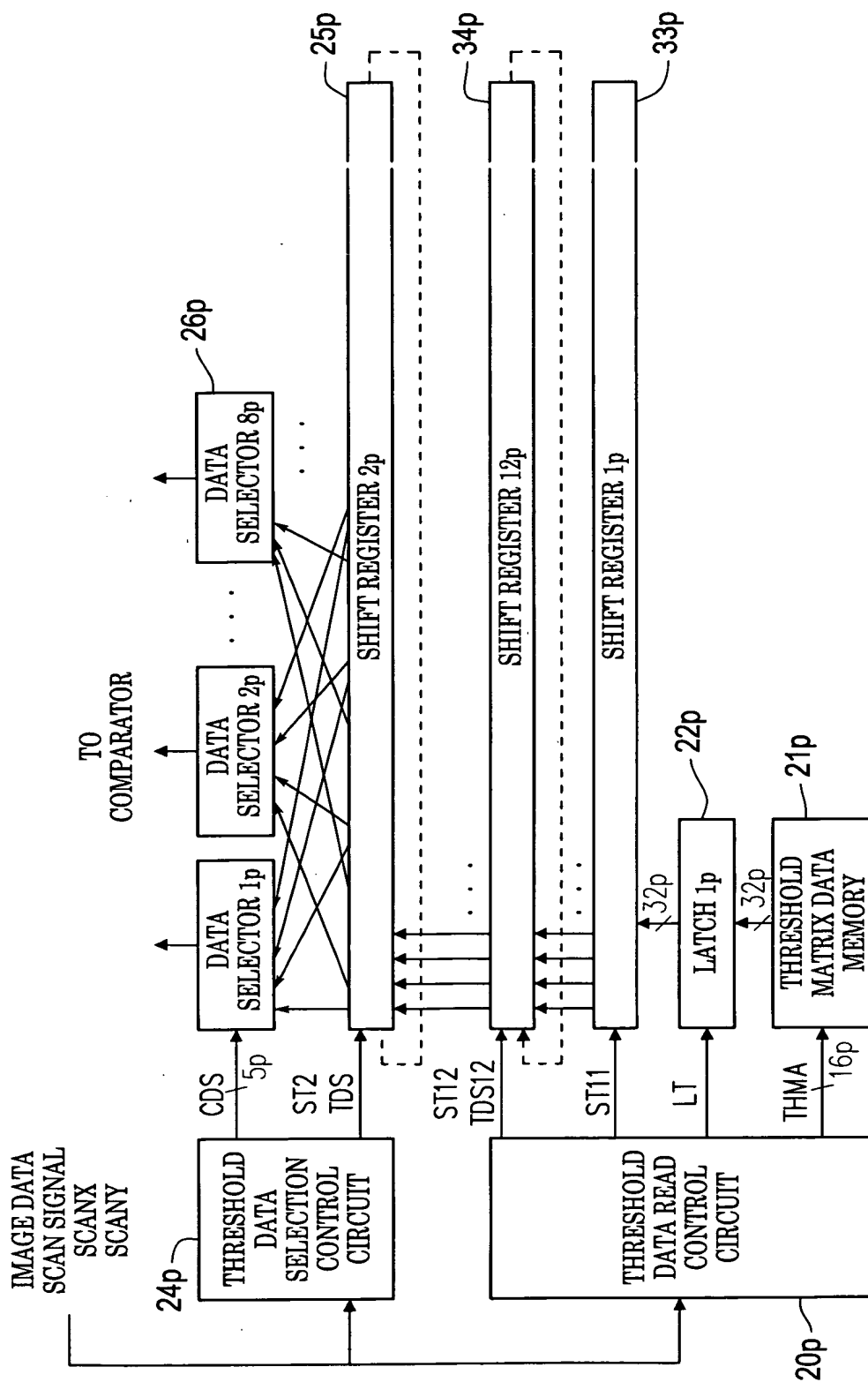
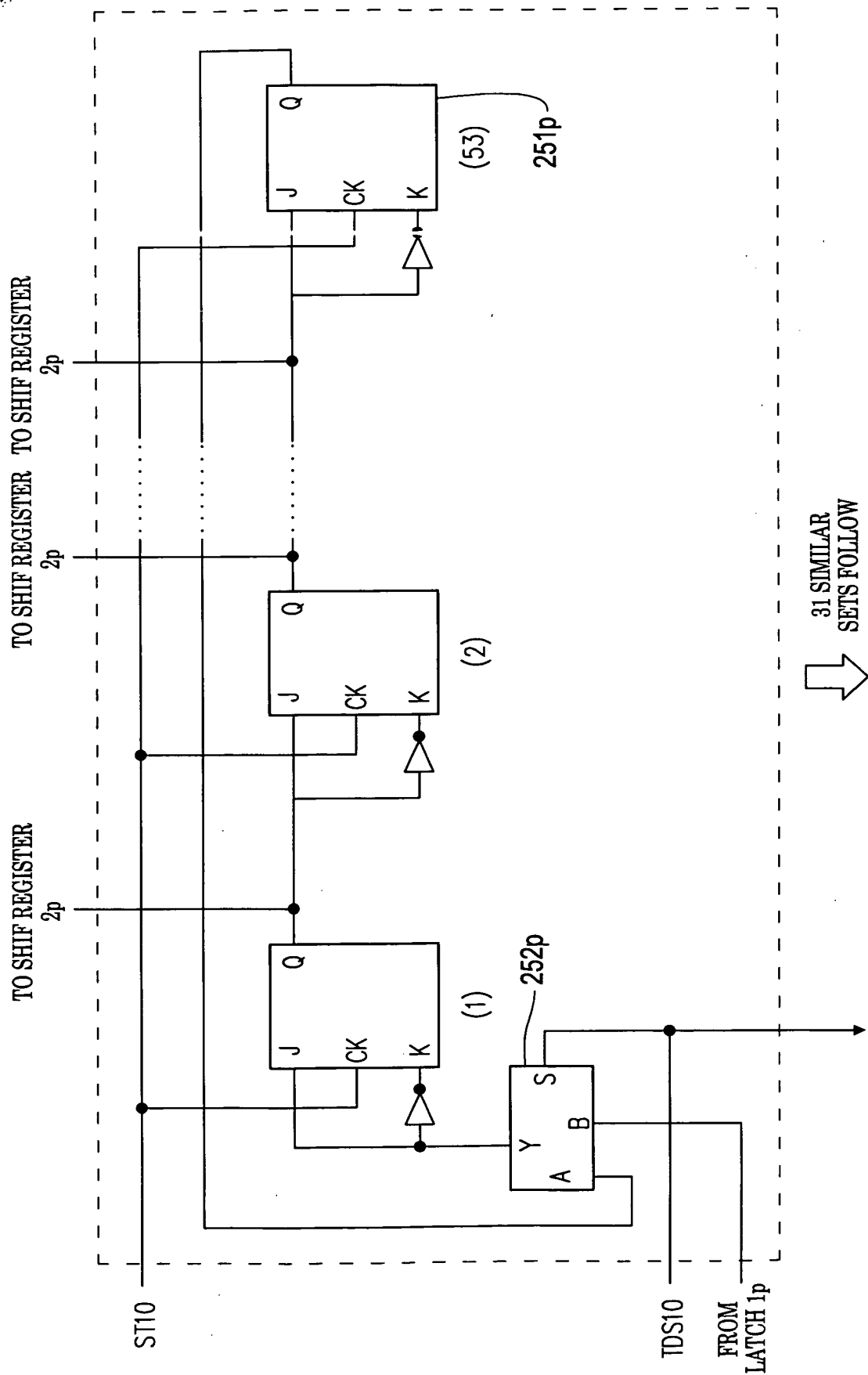


Fig. 10



31 SIMILAR
 SETS FOLLOW

Fig. 11

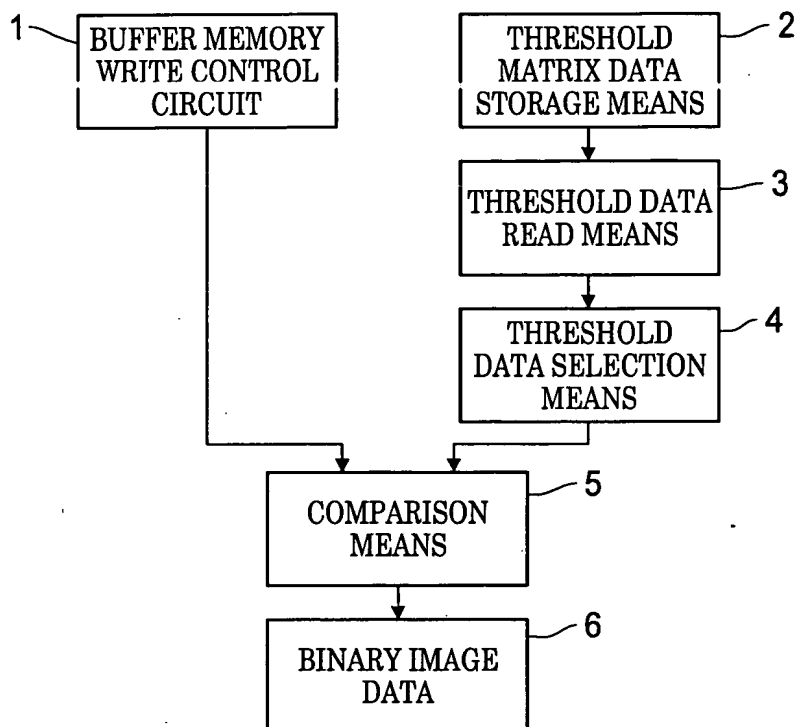


Fig. 12

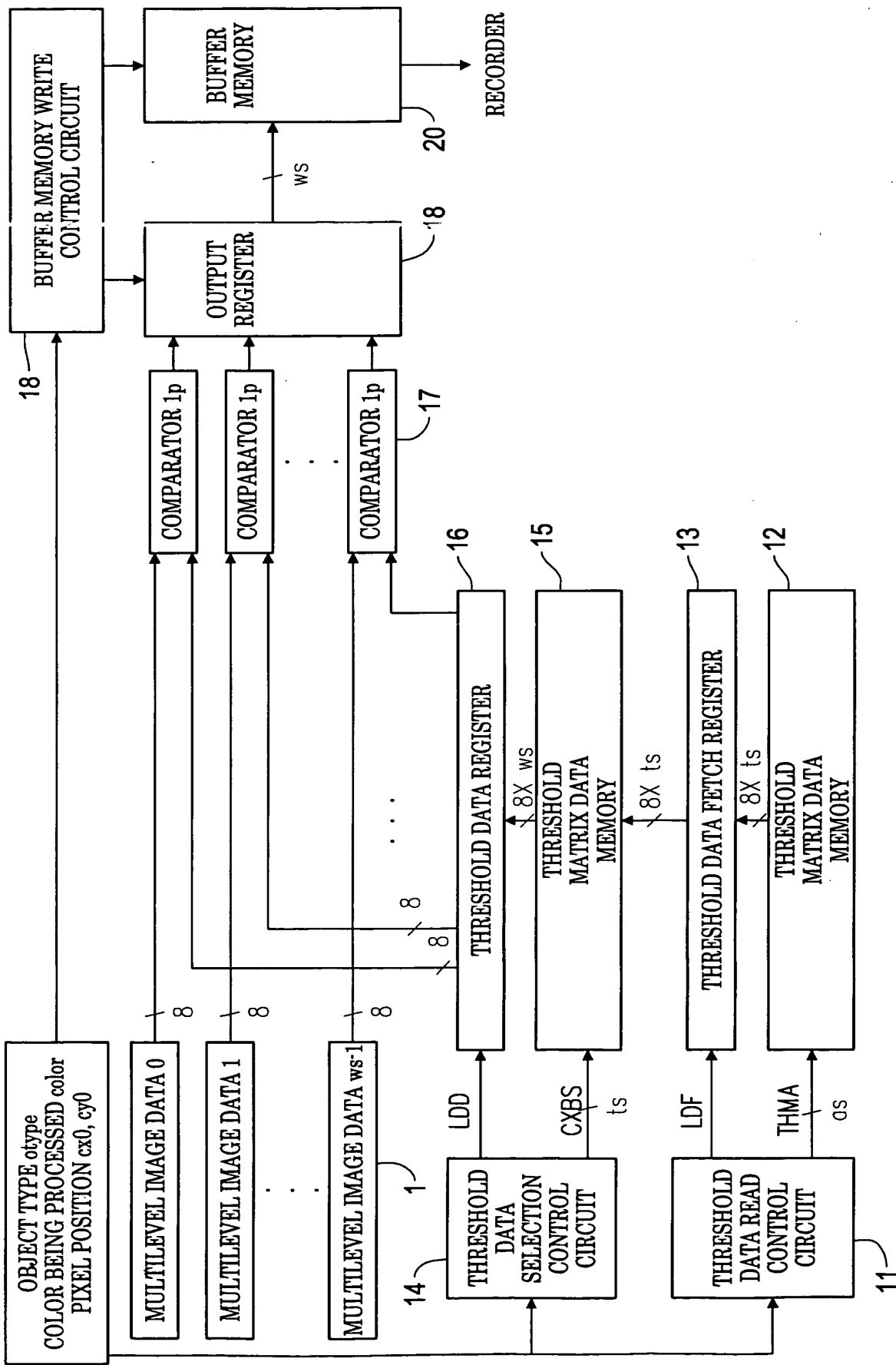


Fig. 13

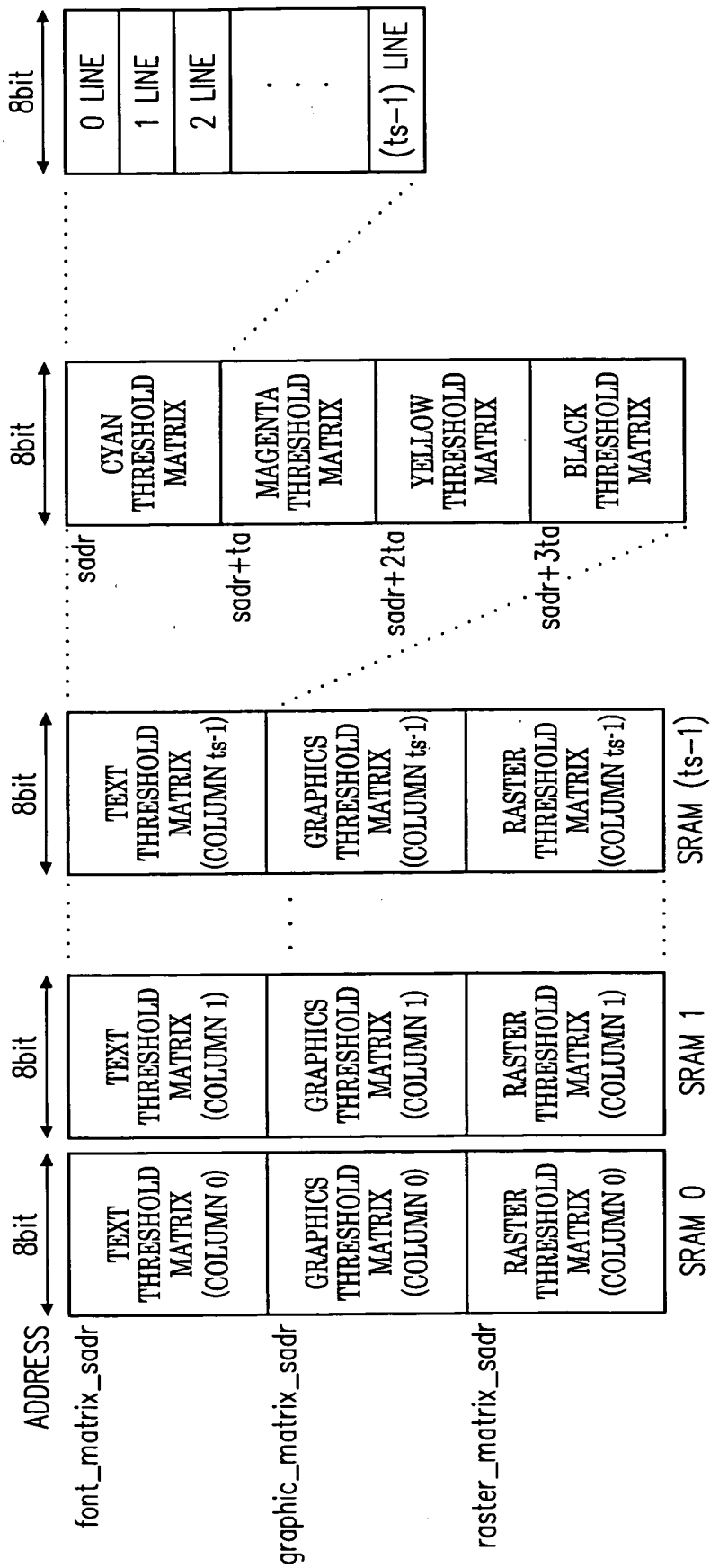


Fig. 14

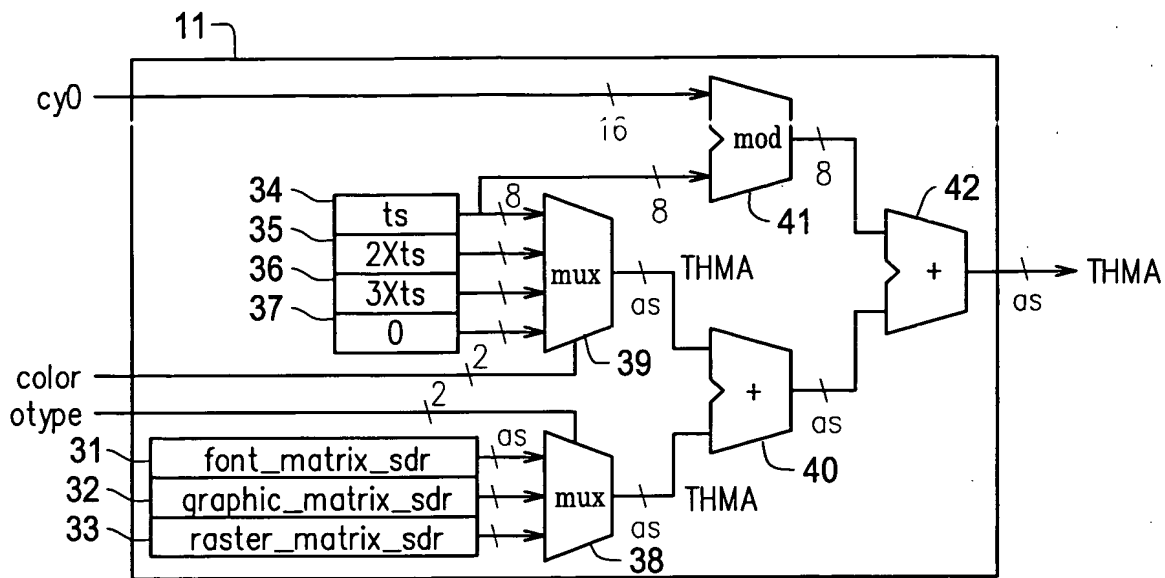


Fig. 15

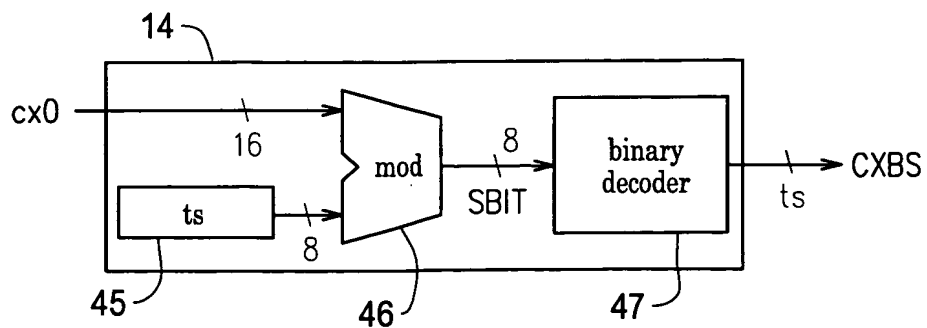


Fig. 16



BINARY DECODER TRUTH TABLE

INPUT (3bit)	OUTPUT (8bit)
000	10000000
001	01000000
010	00100000
011	00010000
100	00001000
101	00000100
110	00000010
111	00000001

Fig. 17

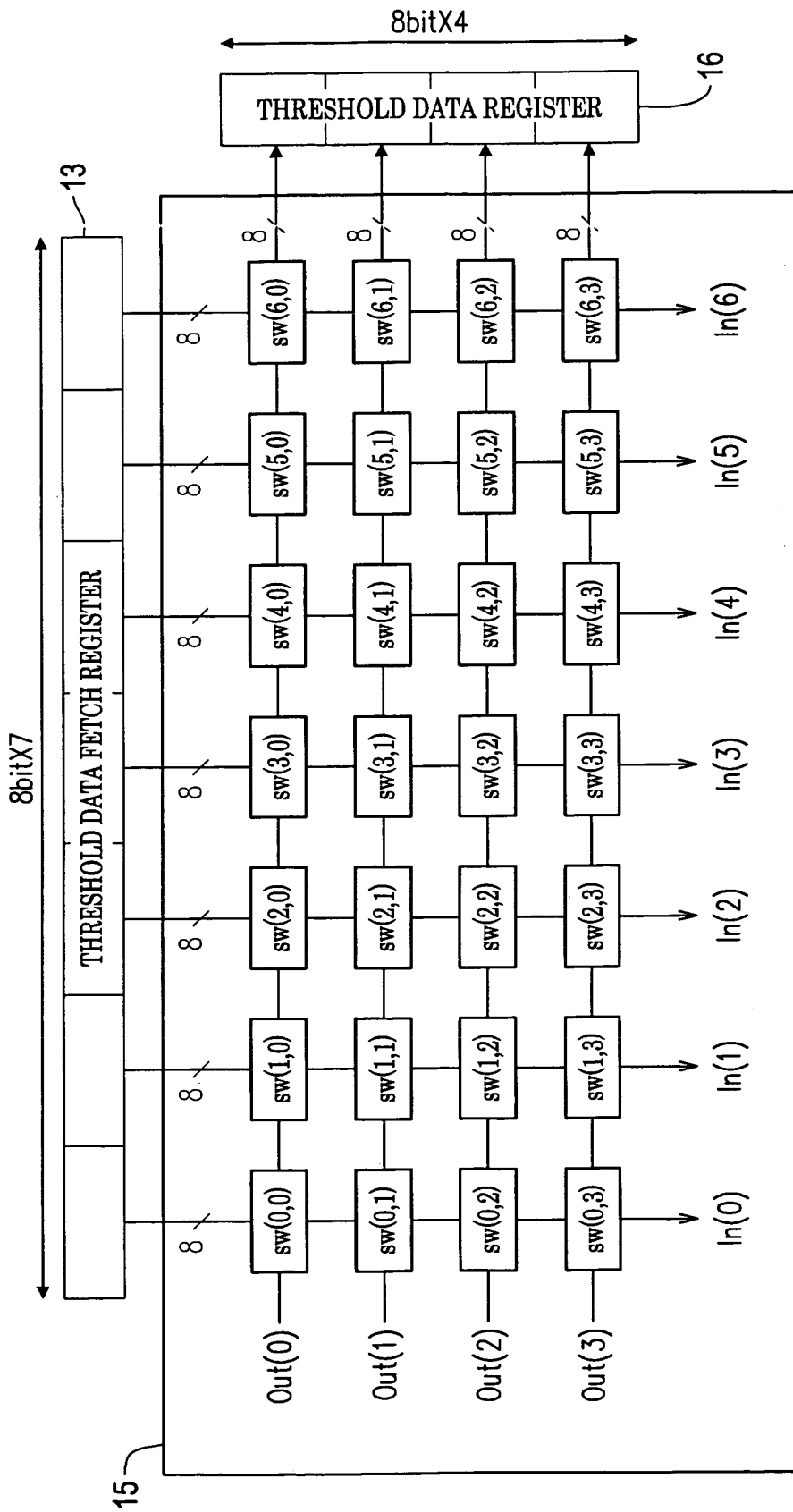


Fig. 18

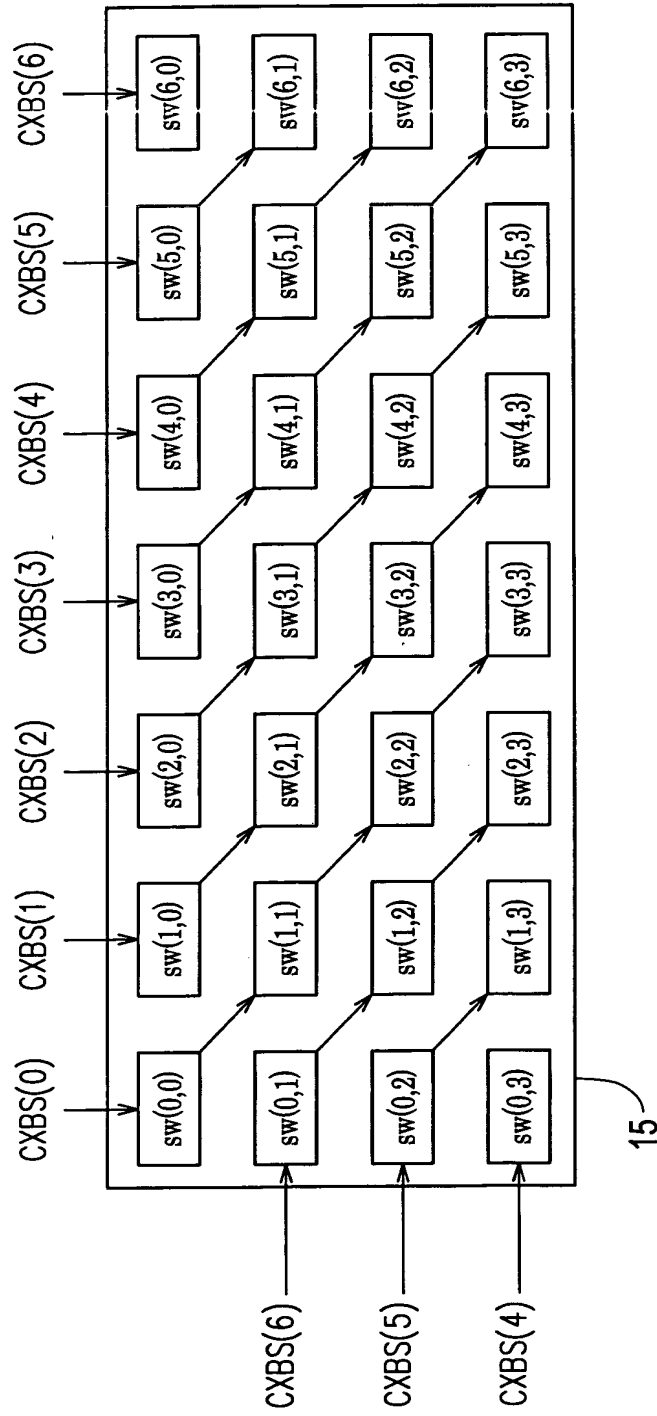


Fig. 19

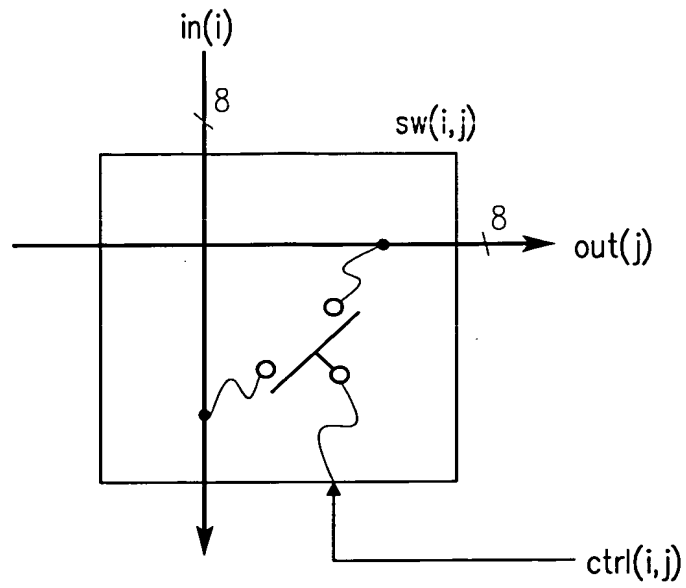


Fig. 20

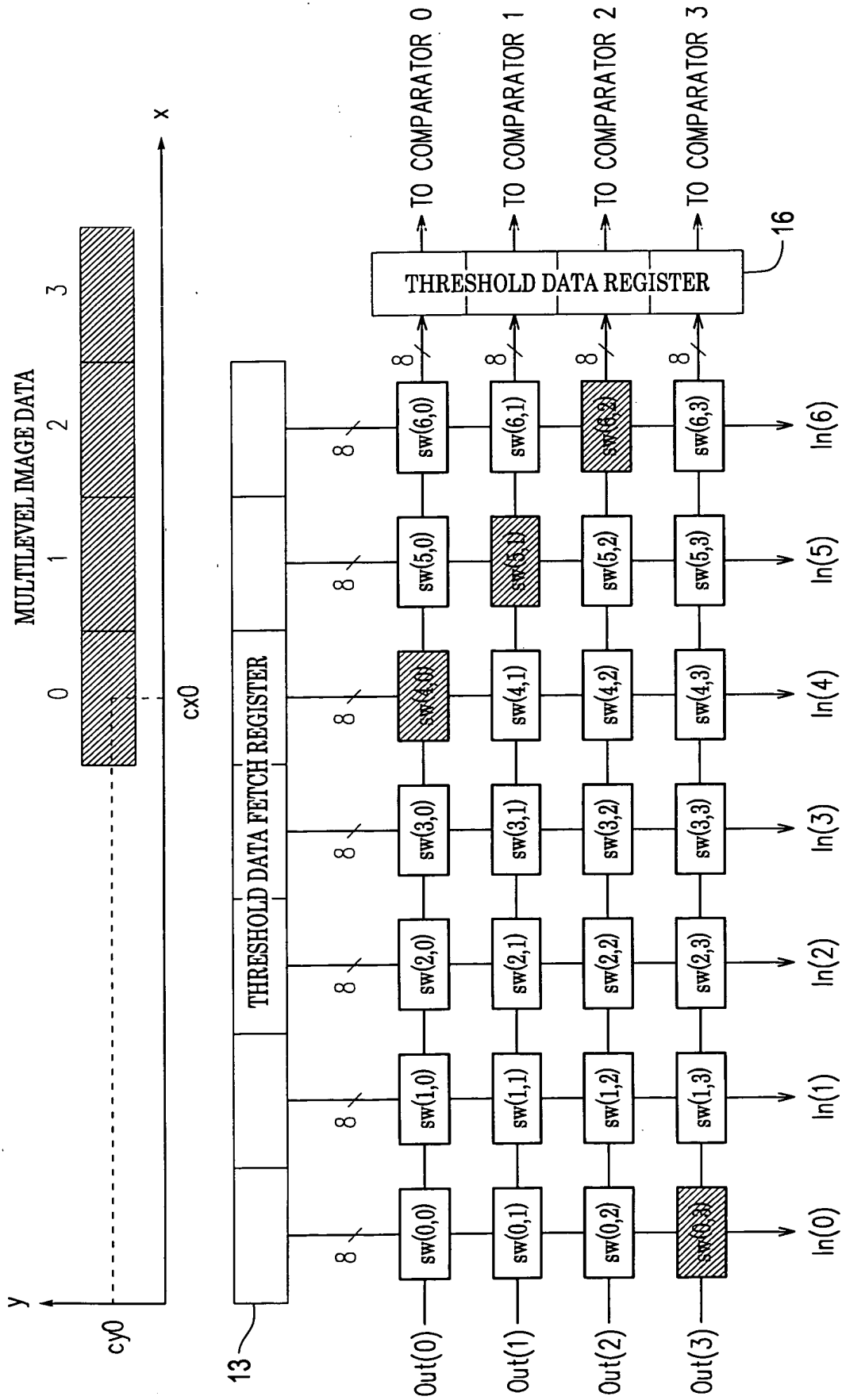


Fig. 21

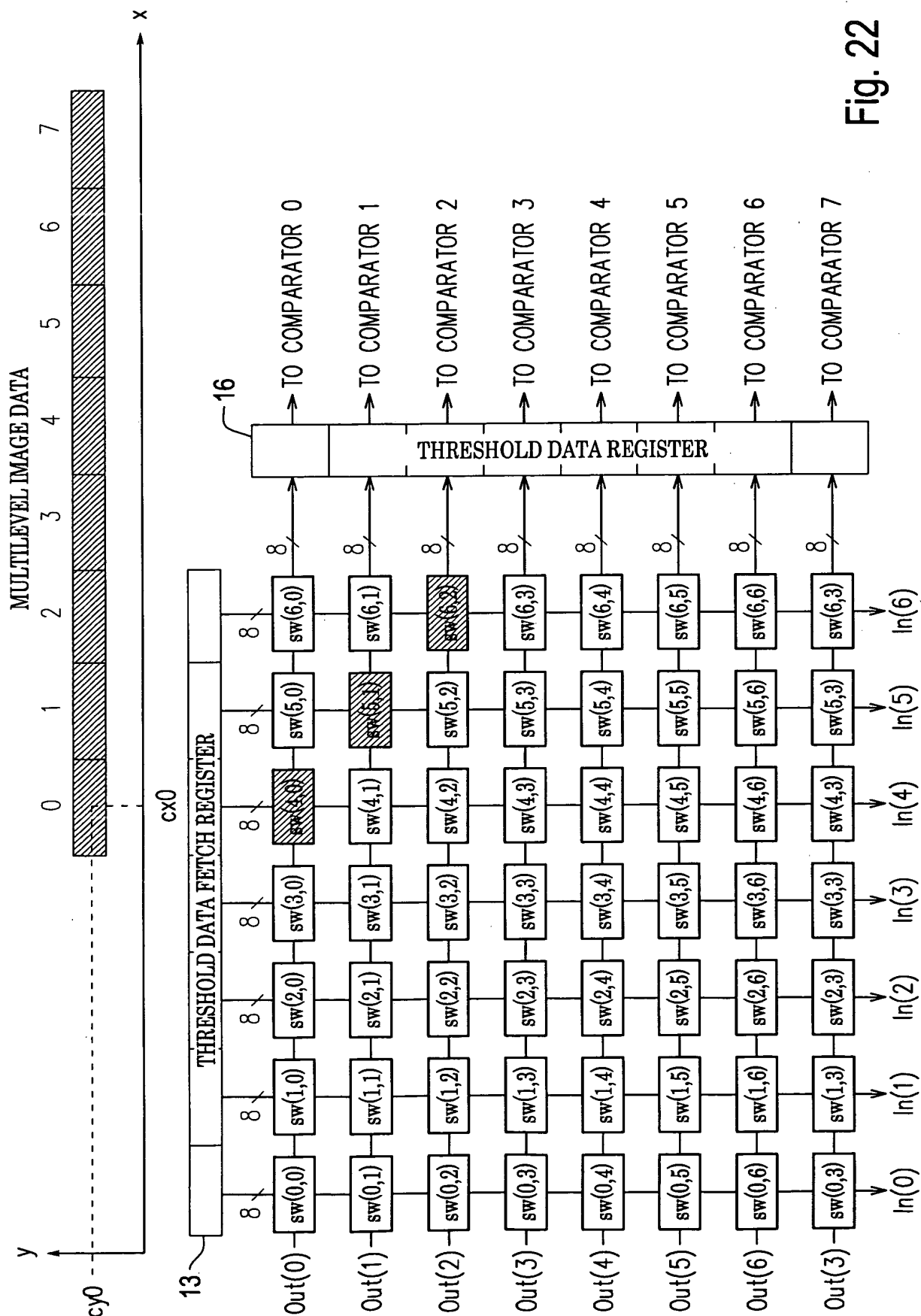


Fig. 22

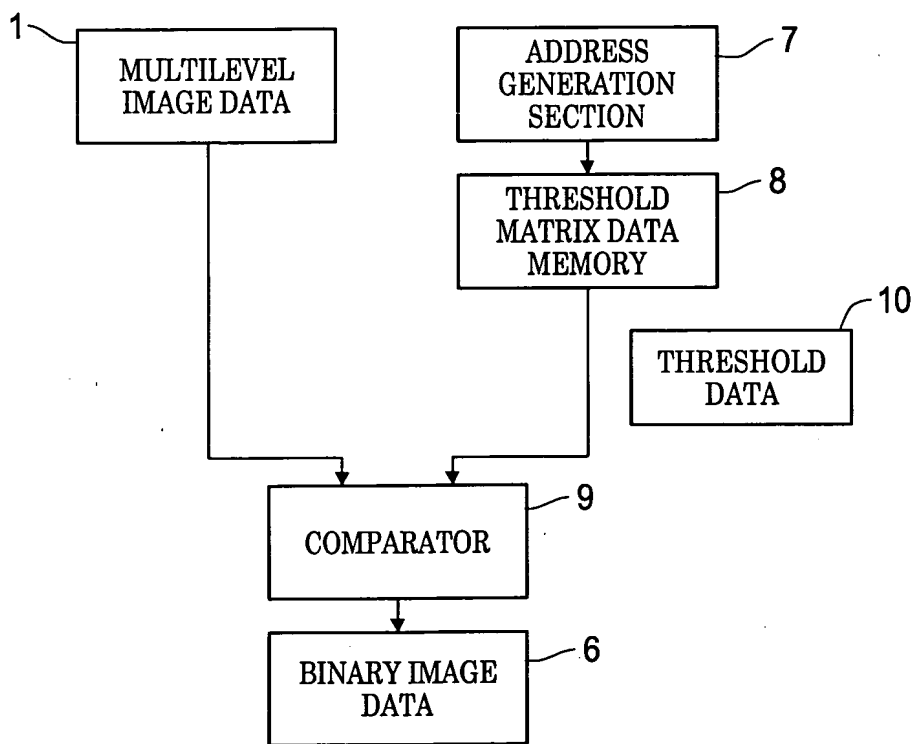


Fig. 23
PRIOR ART

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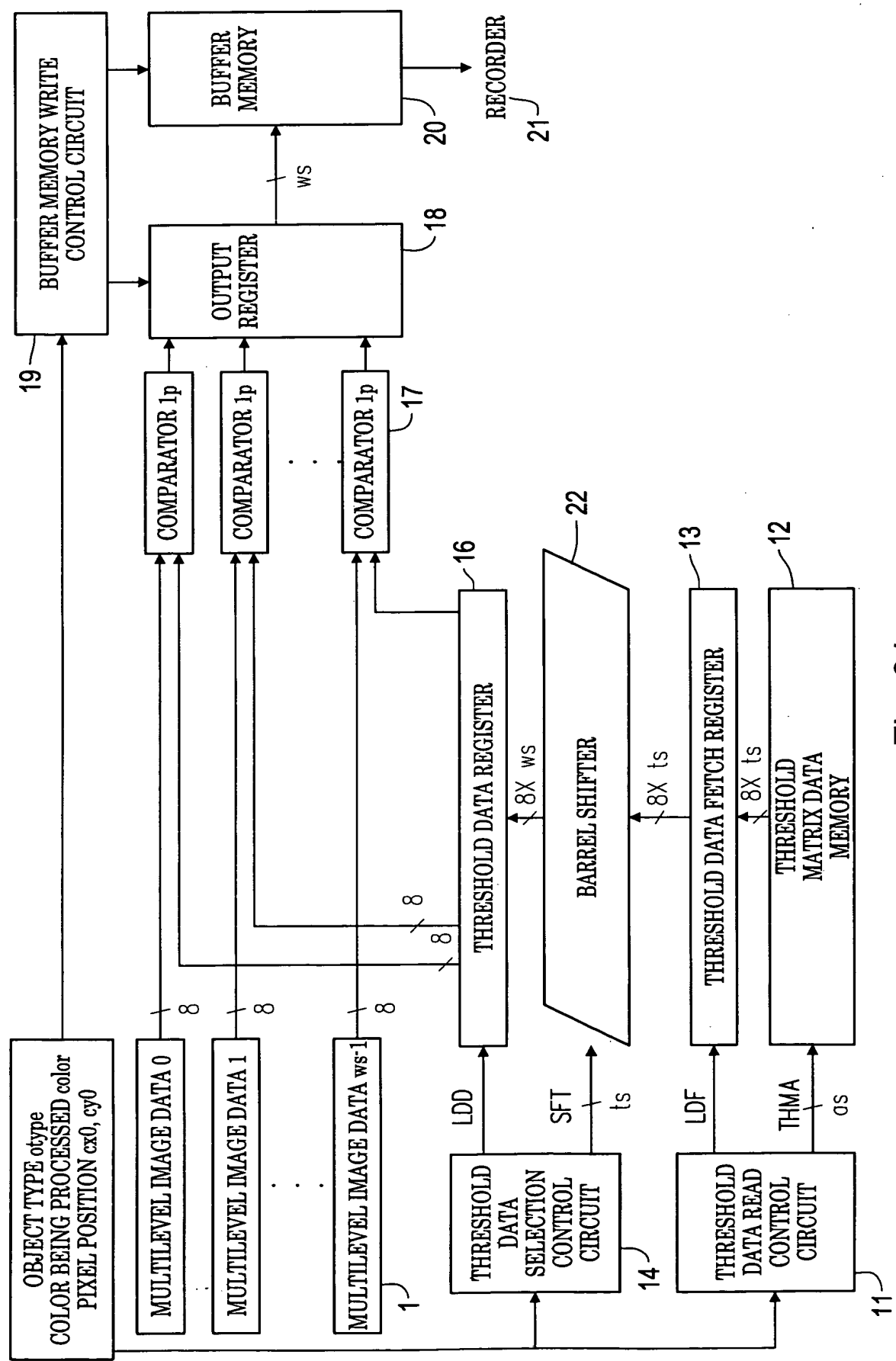


Fig. 24

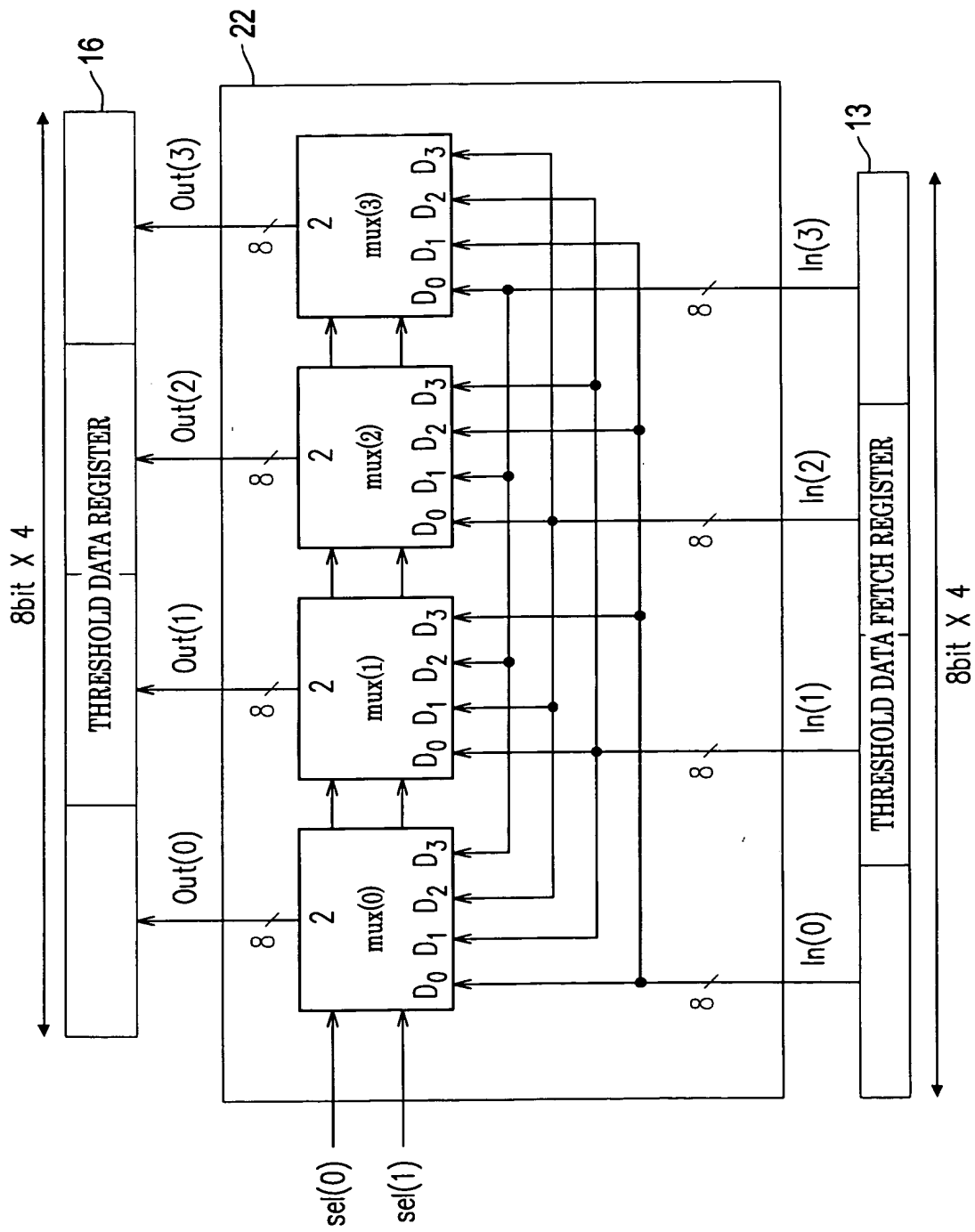


Fig. 25

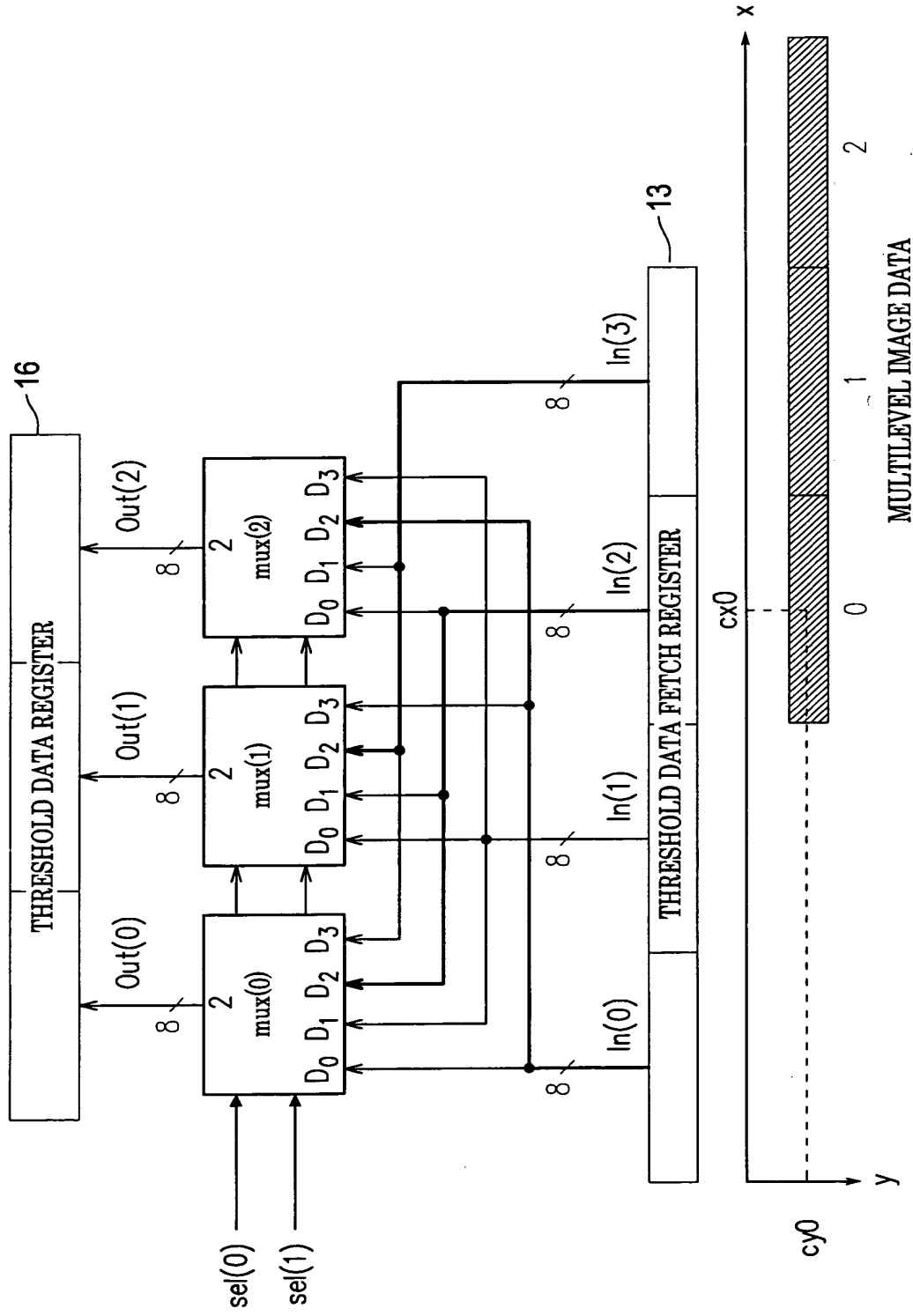


Fig. 26

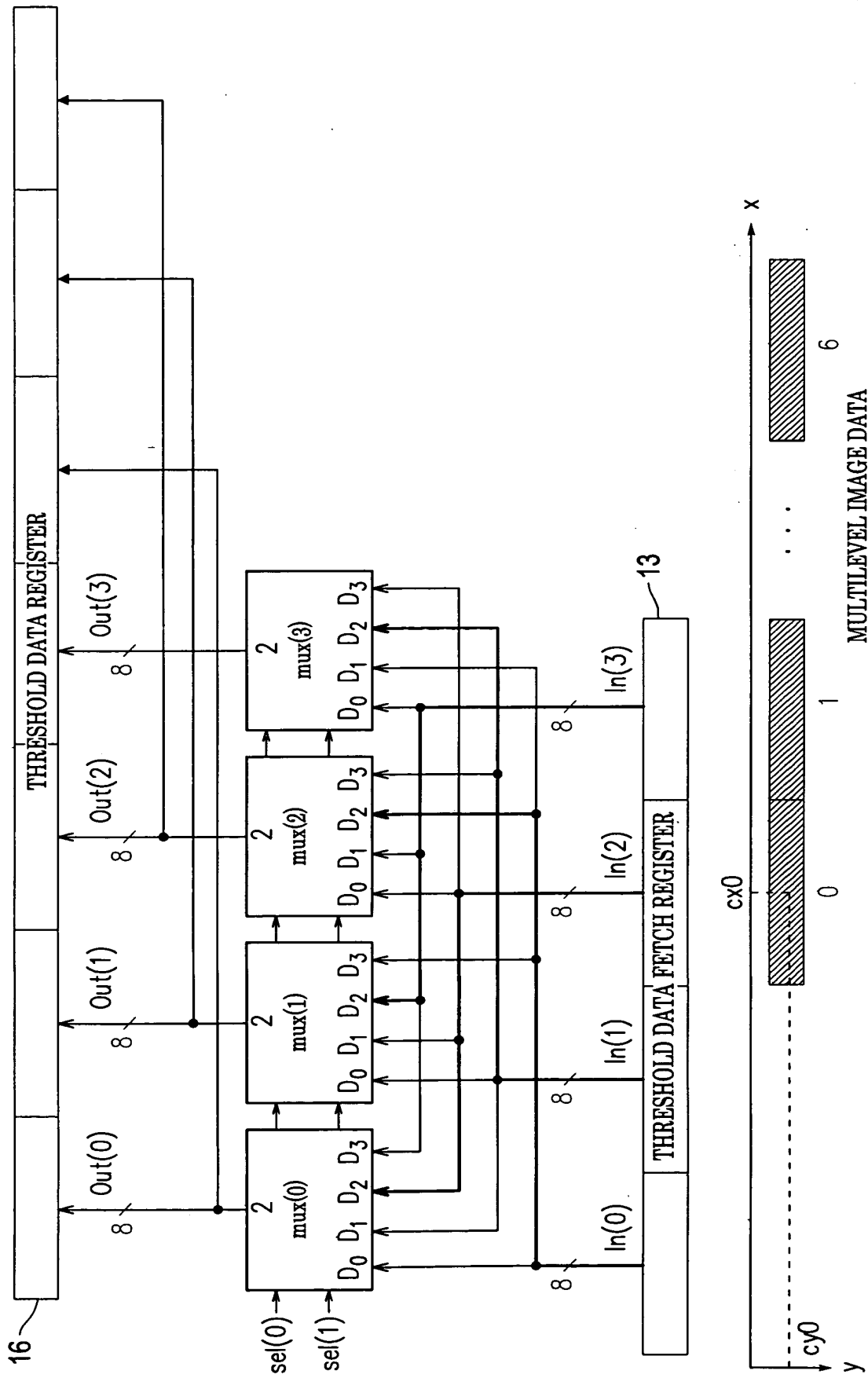


Fig. 27

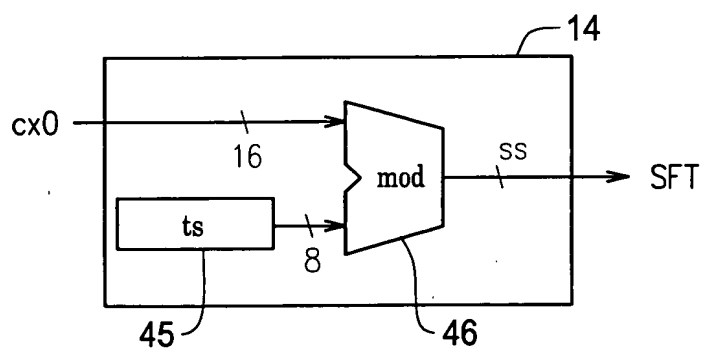


Fig. 28

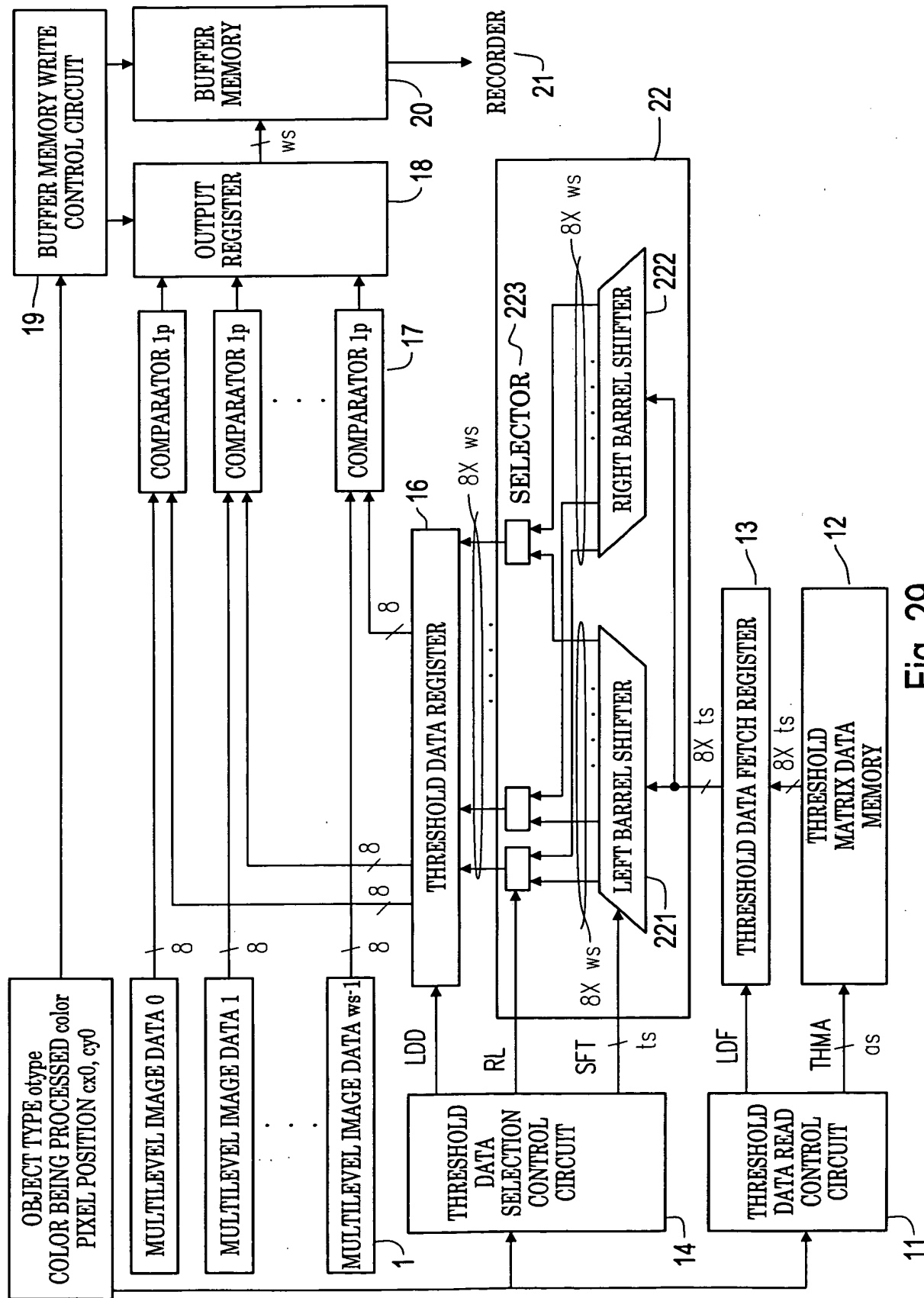


Fig. 29

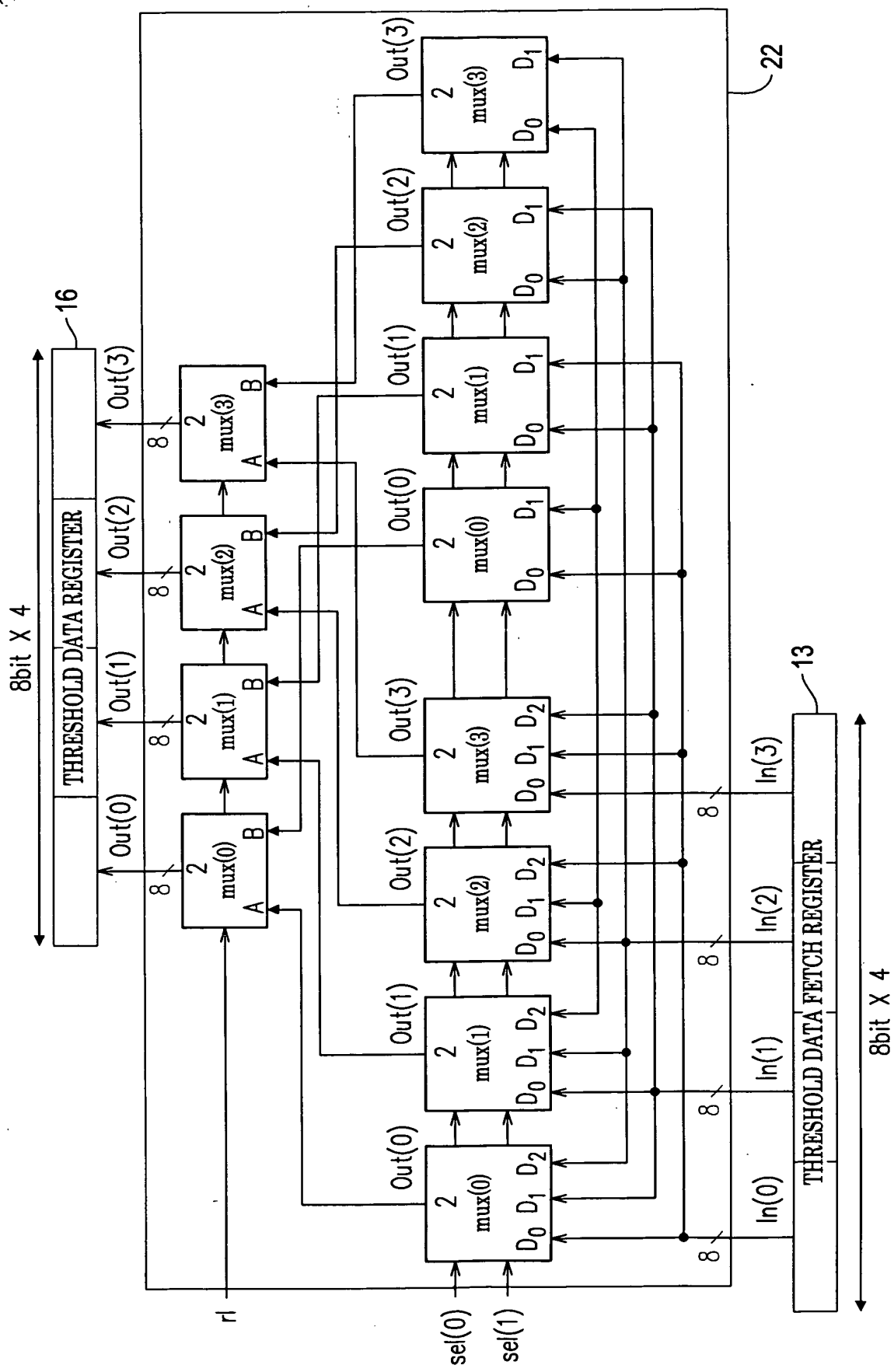


Fig. 30

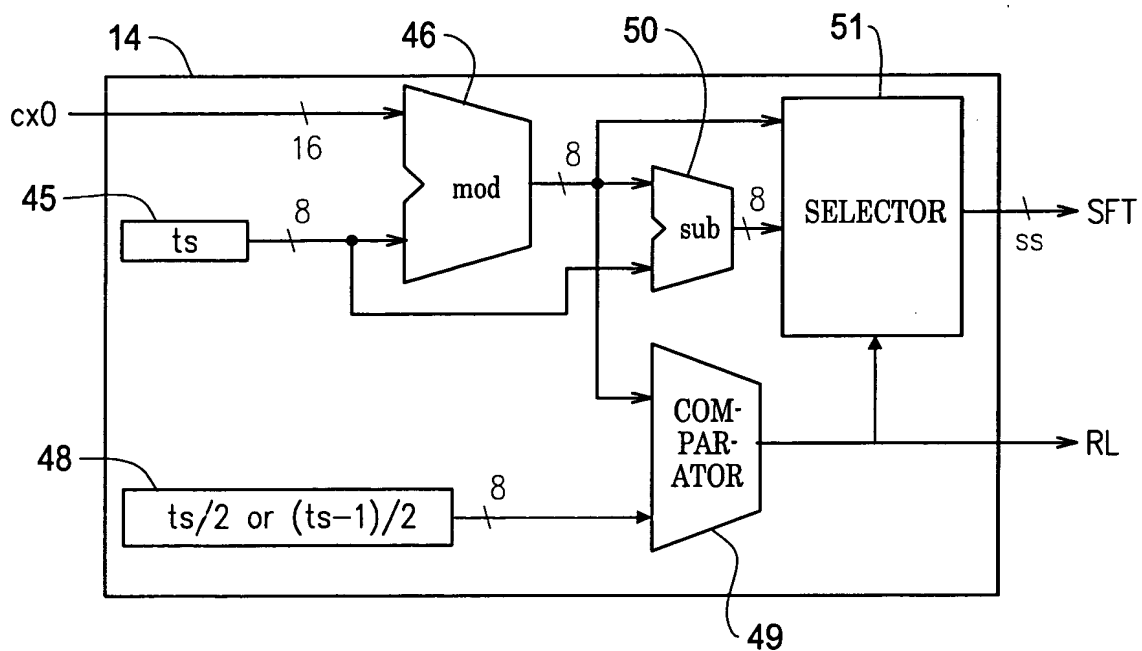


Fig. 31

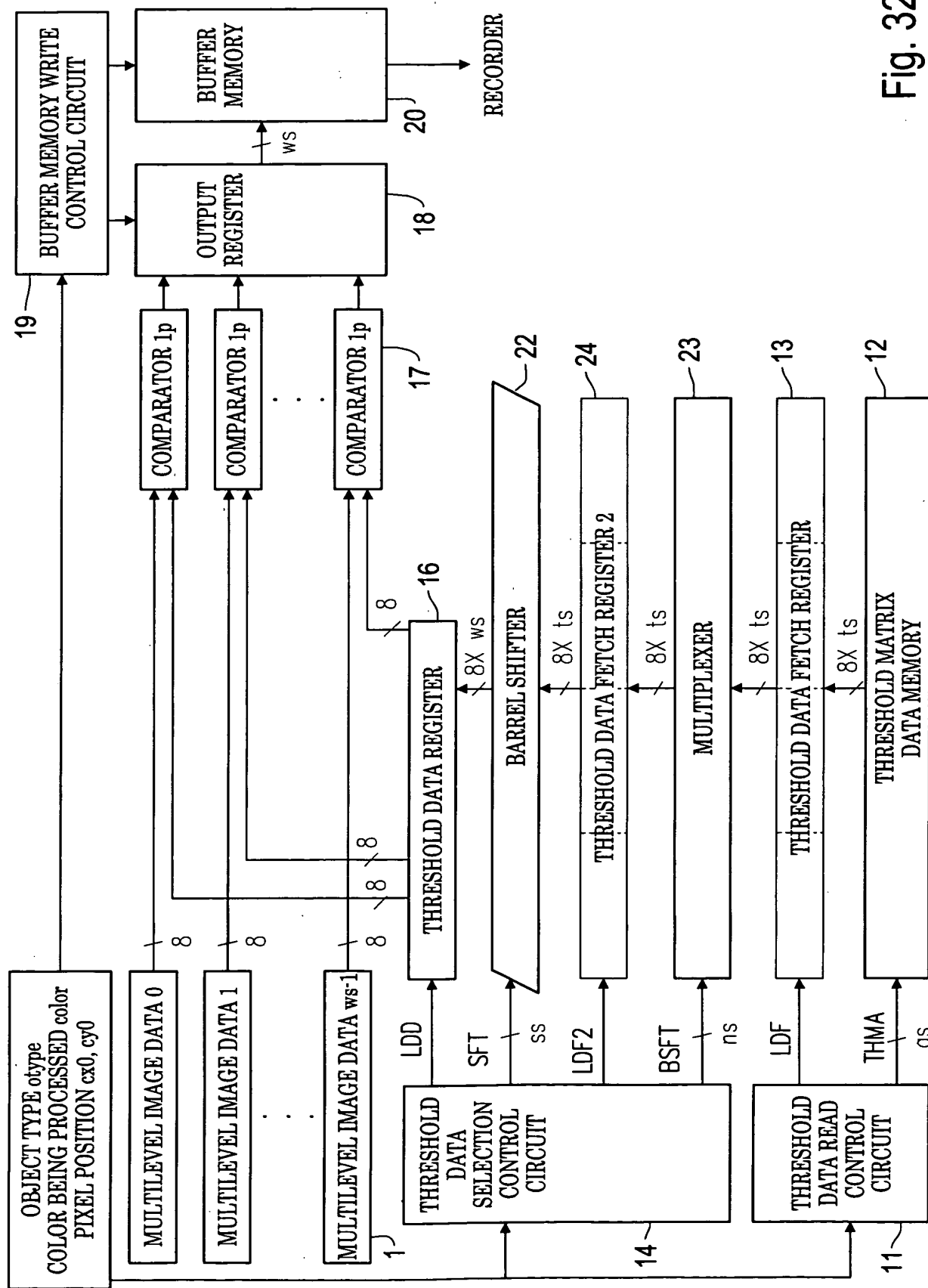


Fig. 32

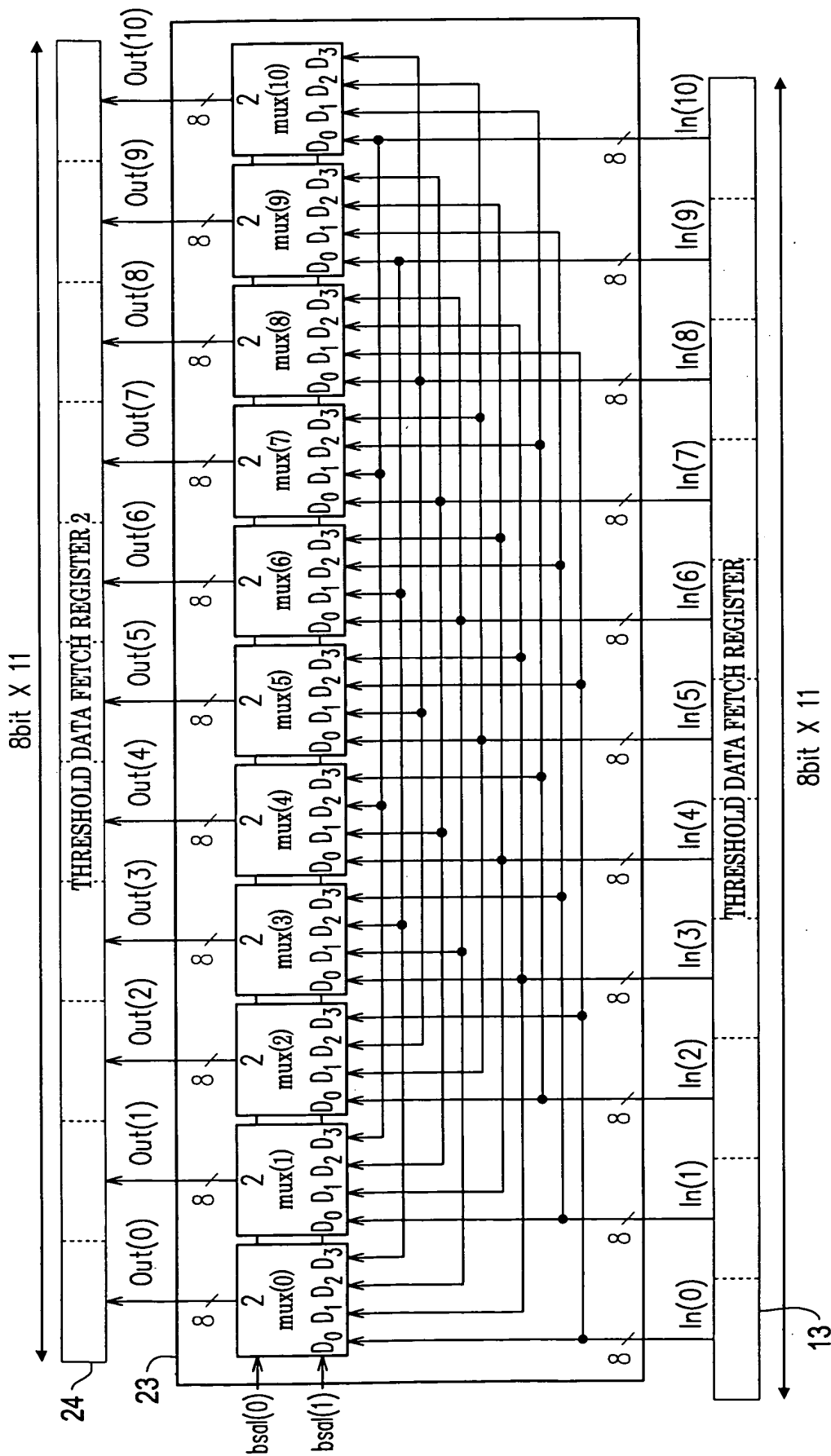


Fig. 33

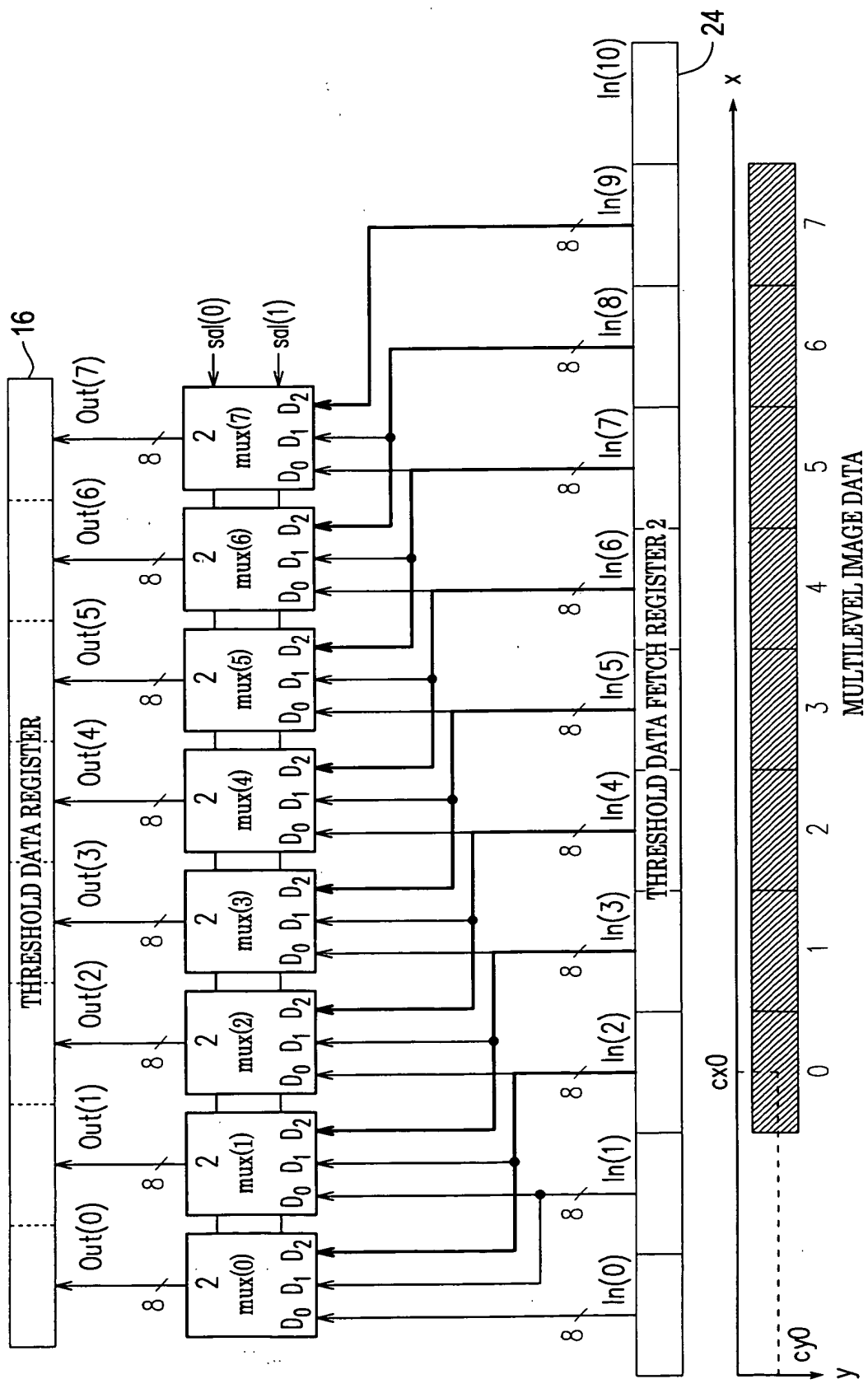


Fig. 34

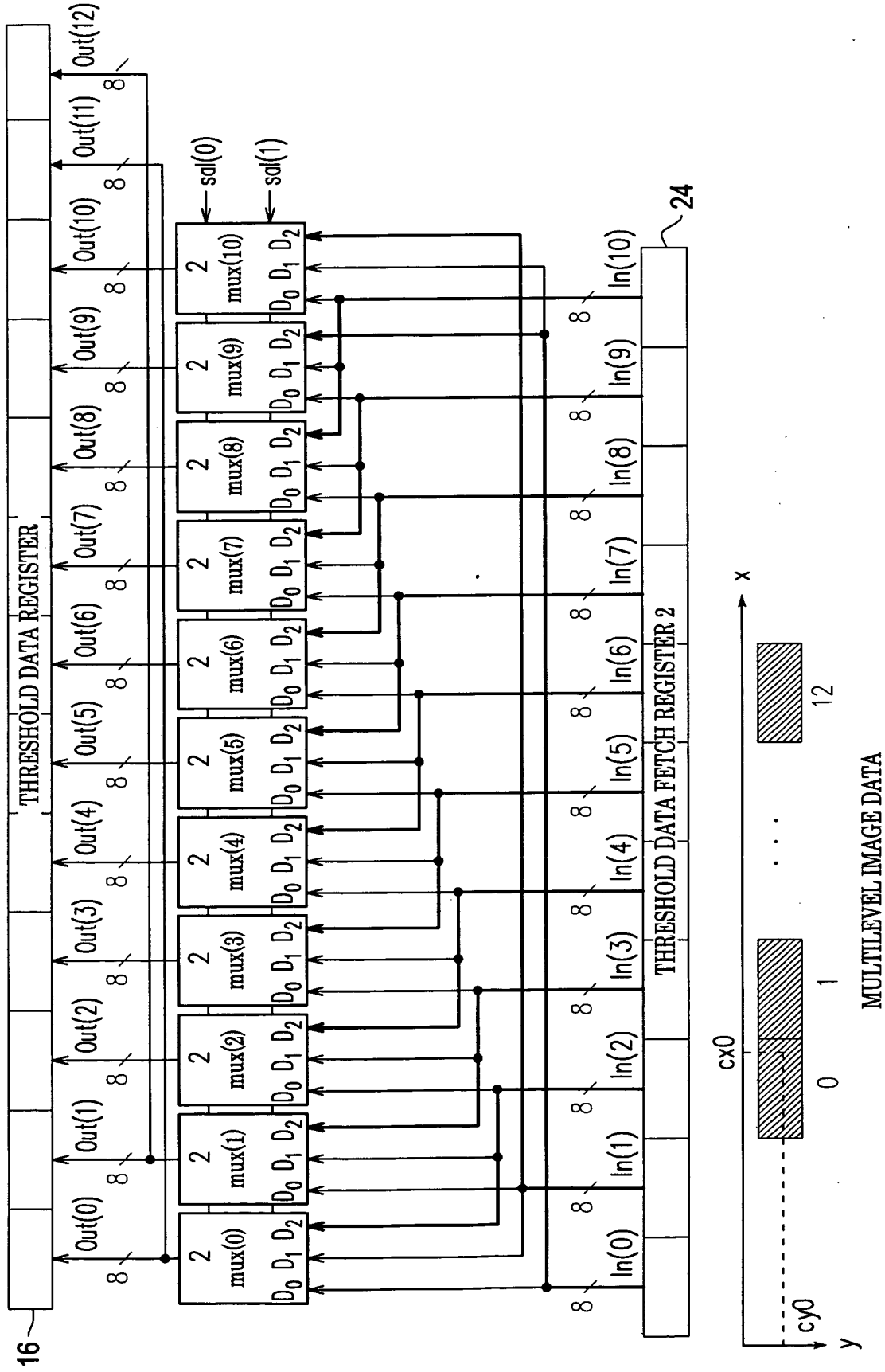


Fig. 35

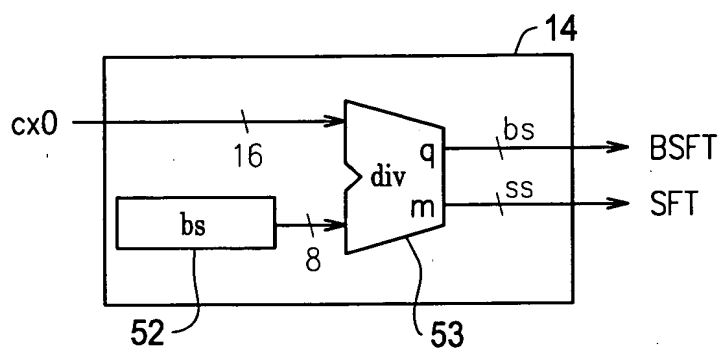


Fig. 36

THRESHOLD MATRIX DATA (MULTILEVEL)

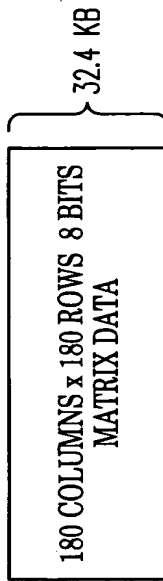


Fig. 37A

BINARY MATRIX DATA

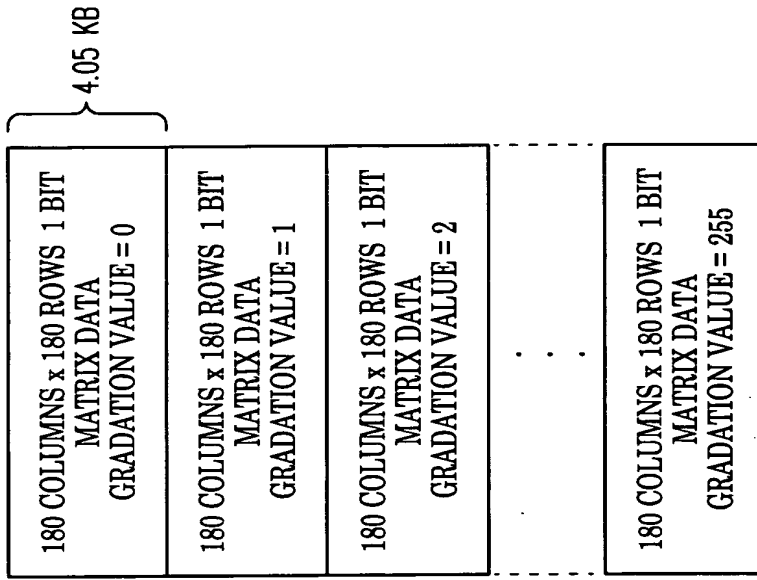


Fig. 37B



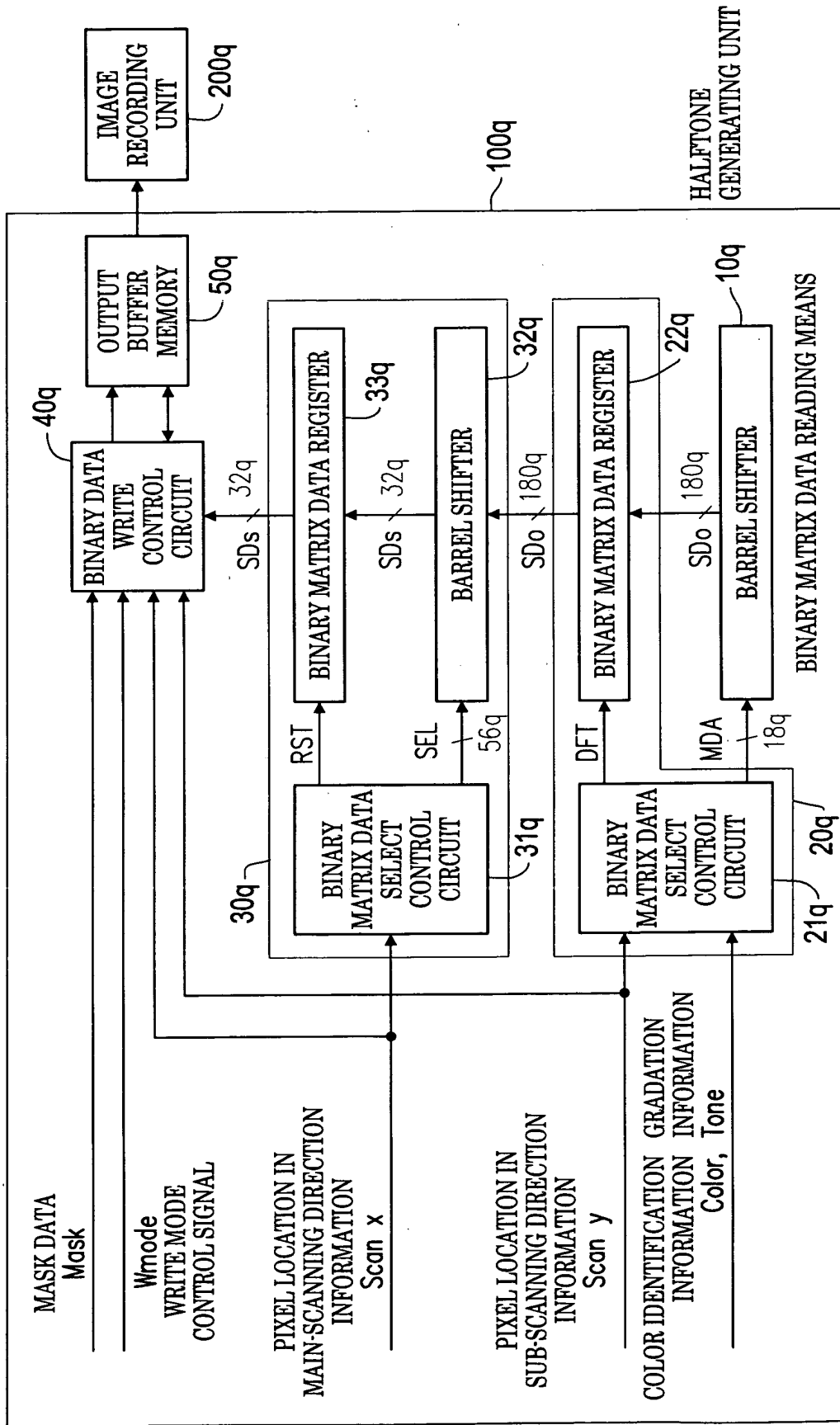


Fig. 38

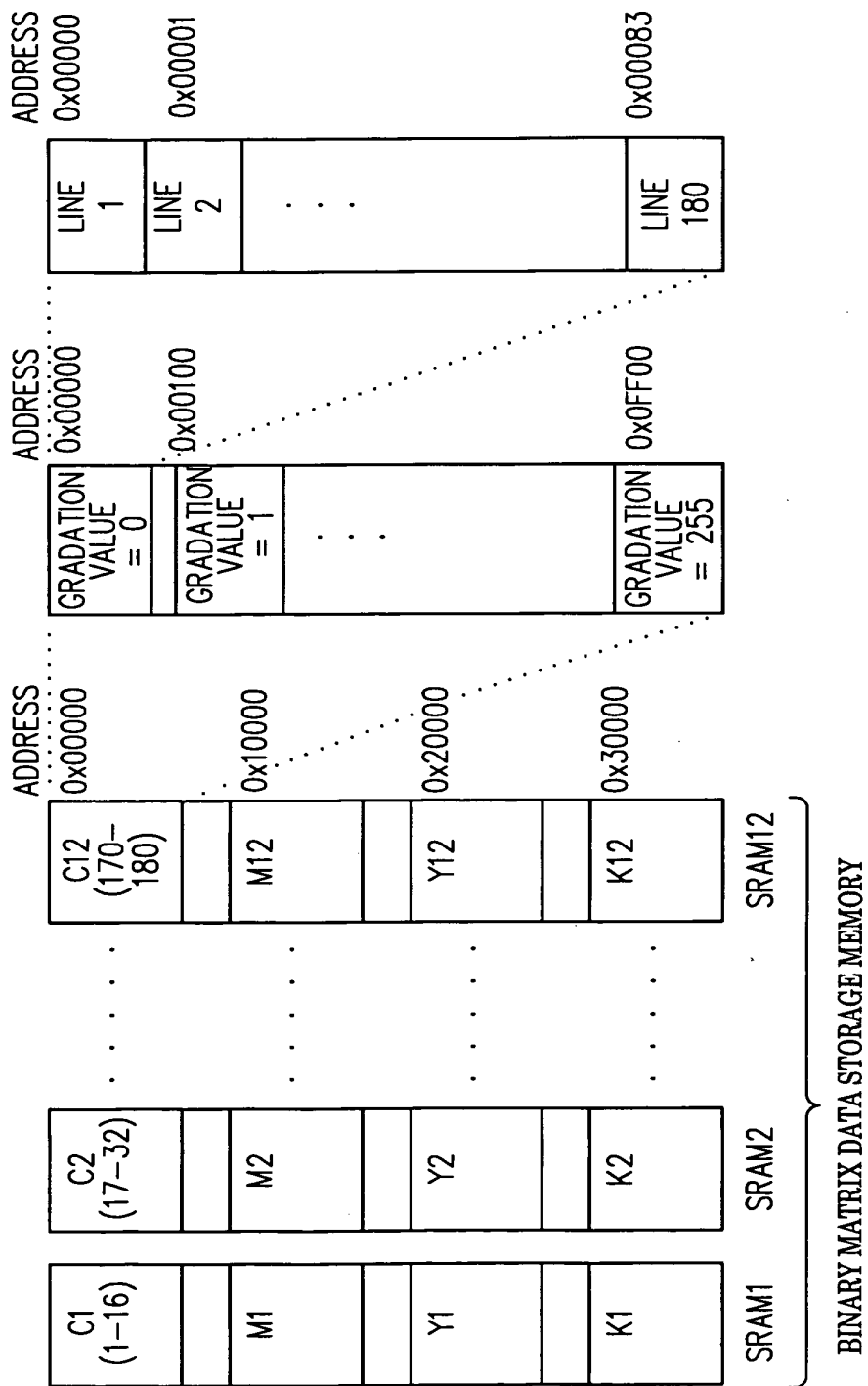


Fig. 39

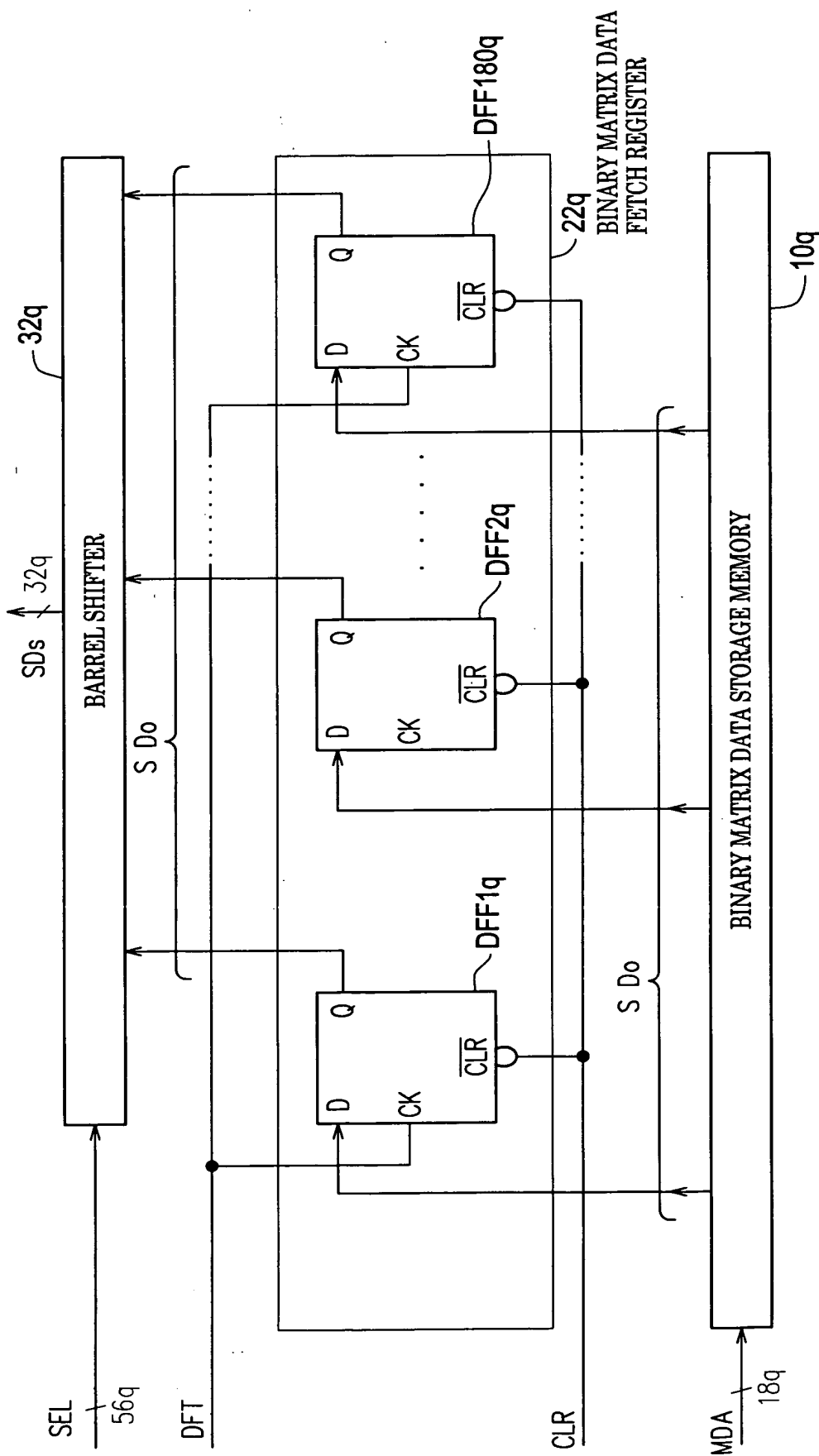


Fig. 40

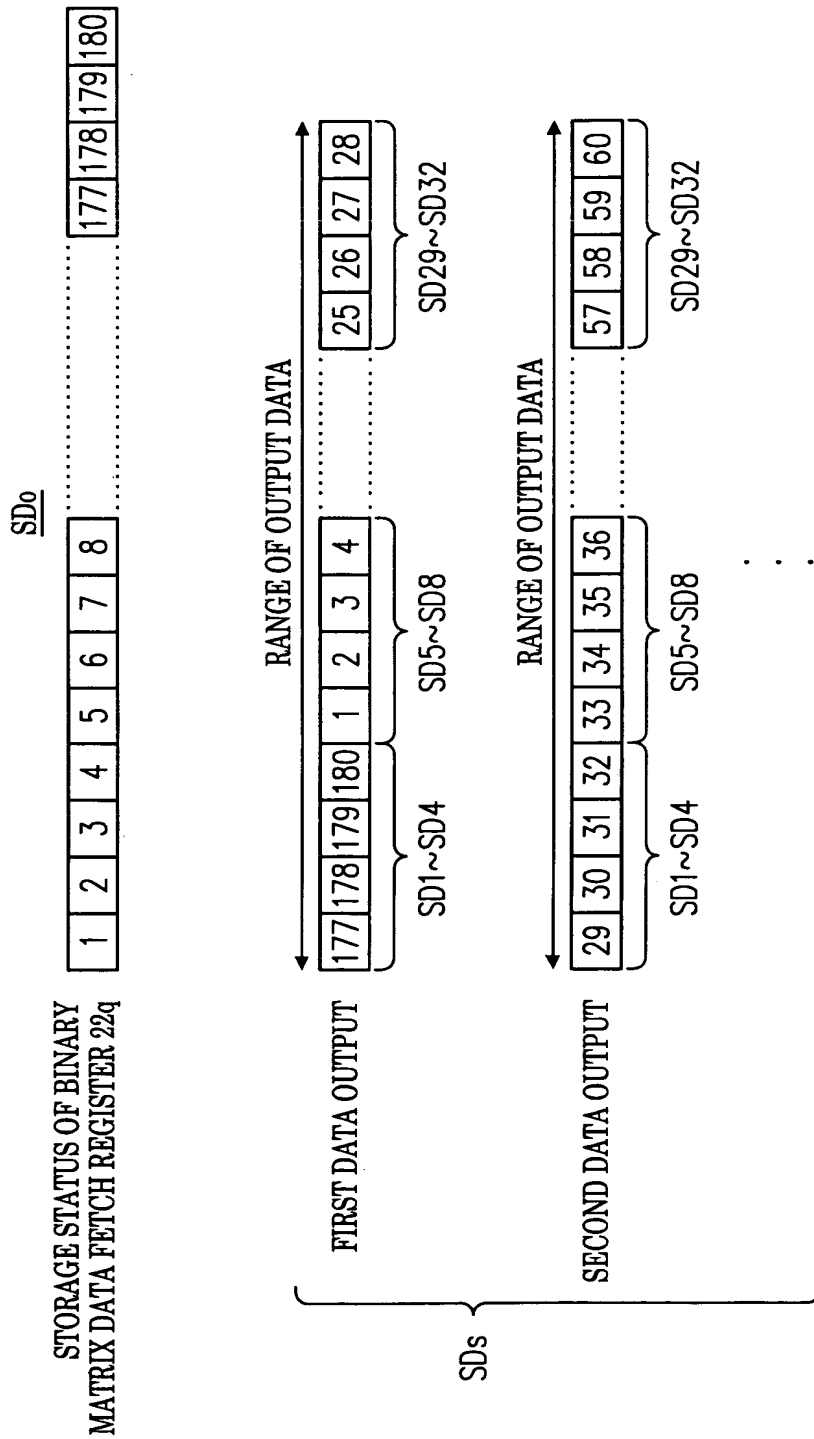


Fig. 41

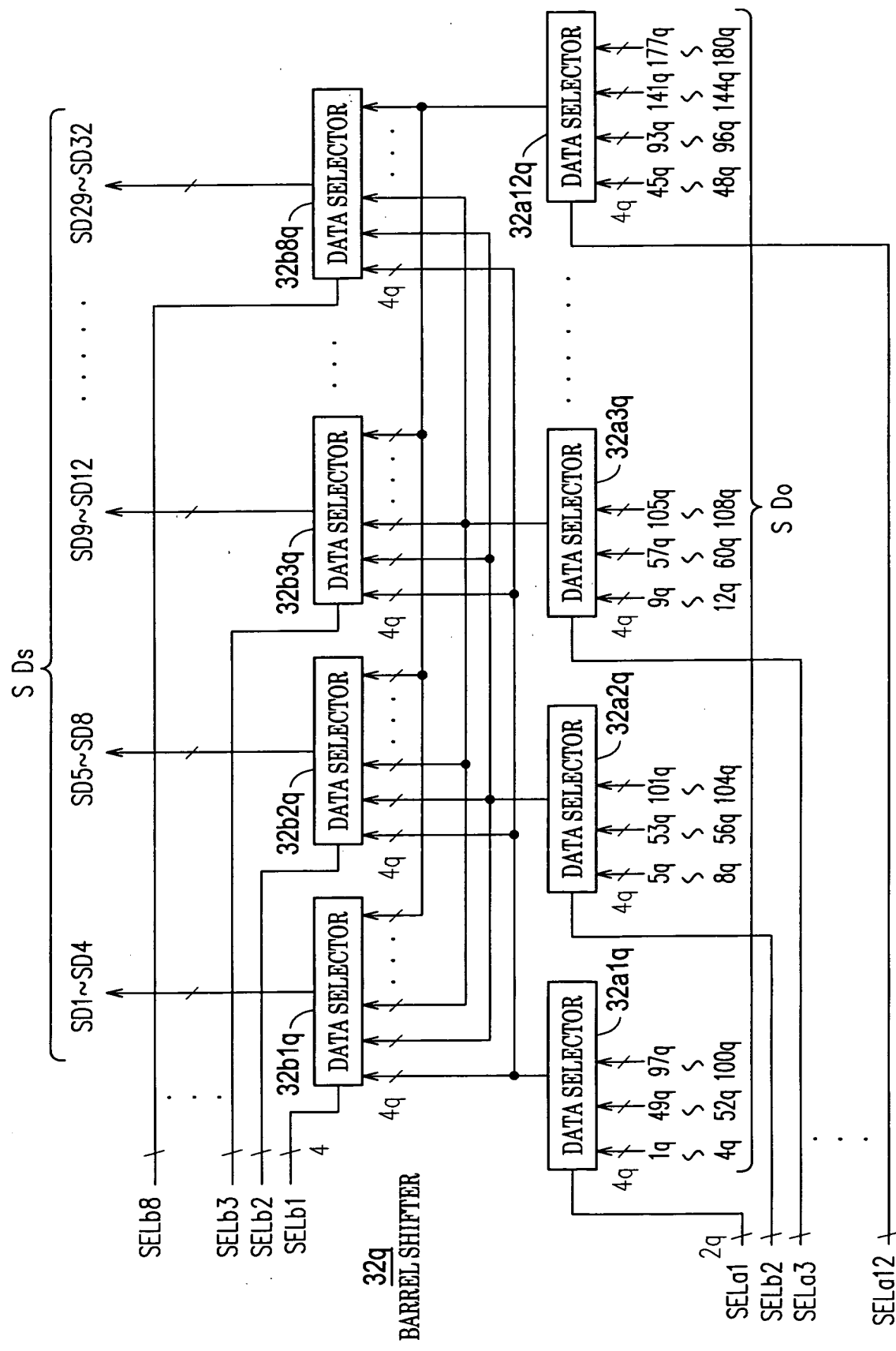


Fig. 42

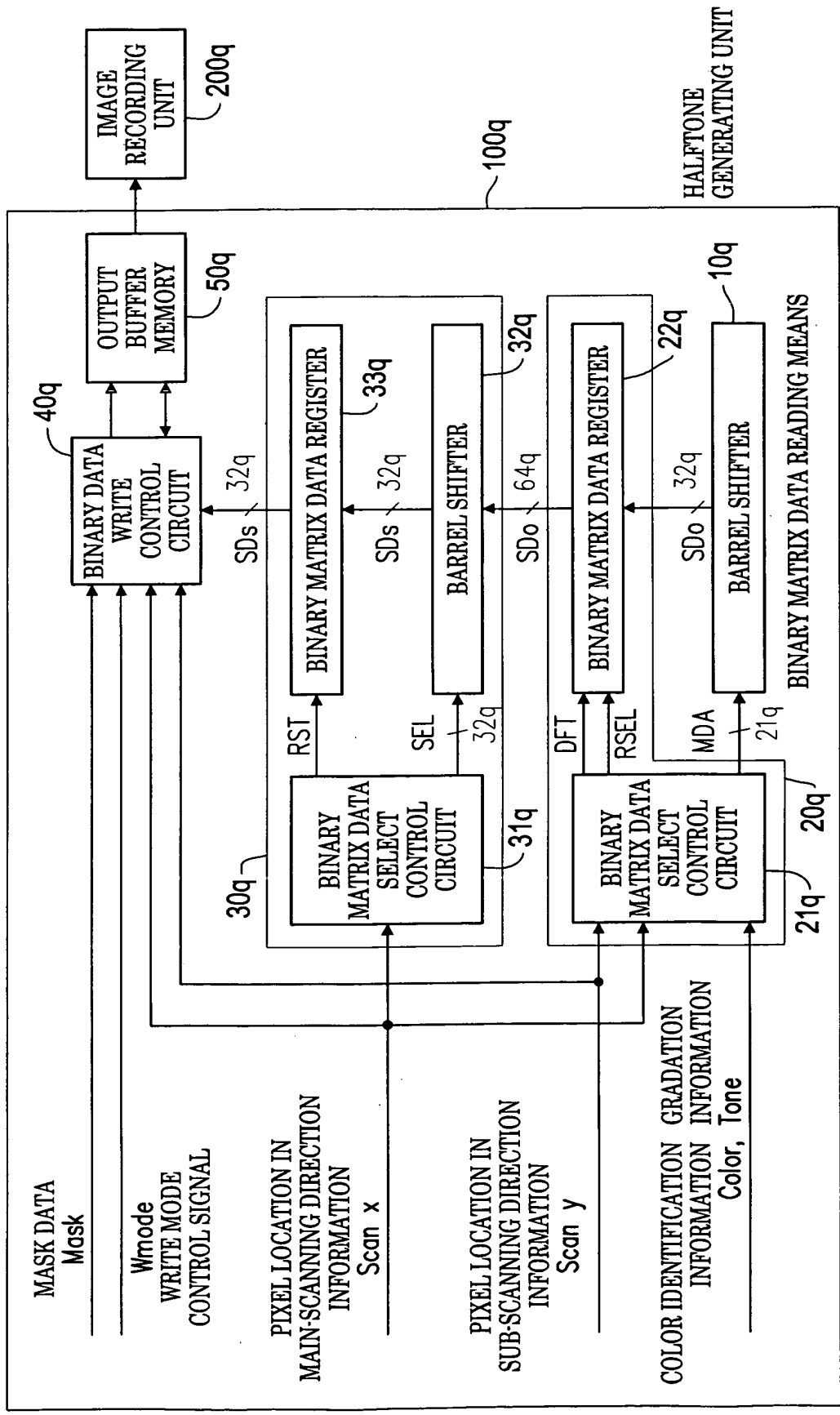


Fig. 43

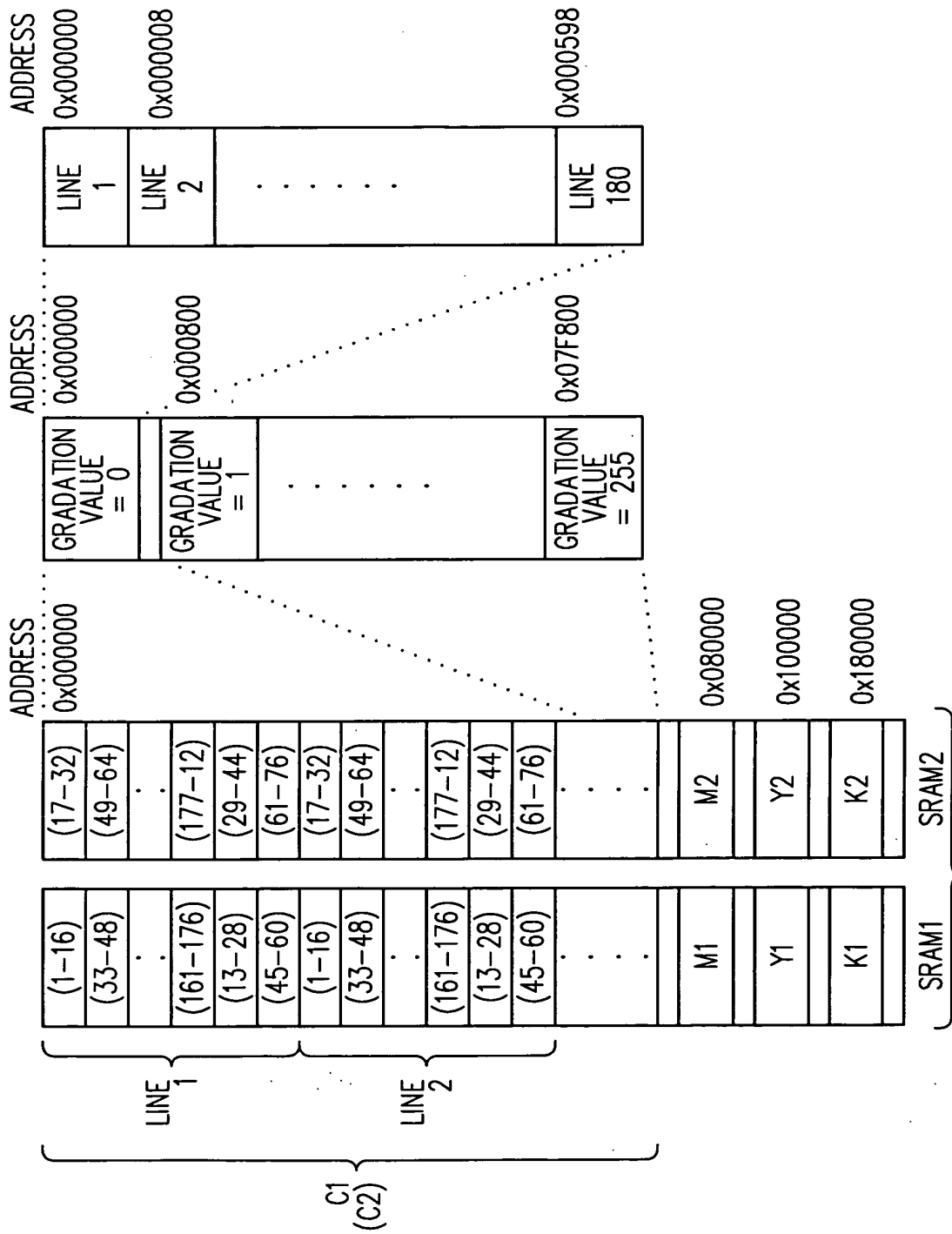


Fig. 44

BINARY MATRIX DATA STORAGE MEMORY

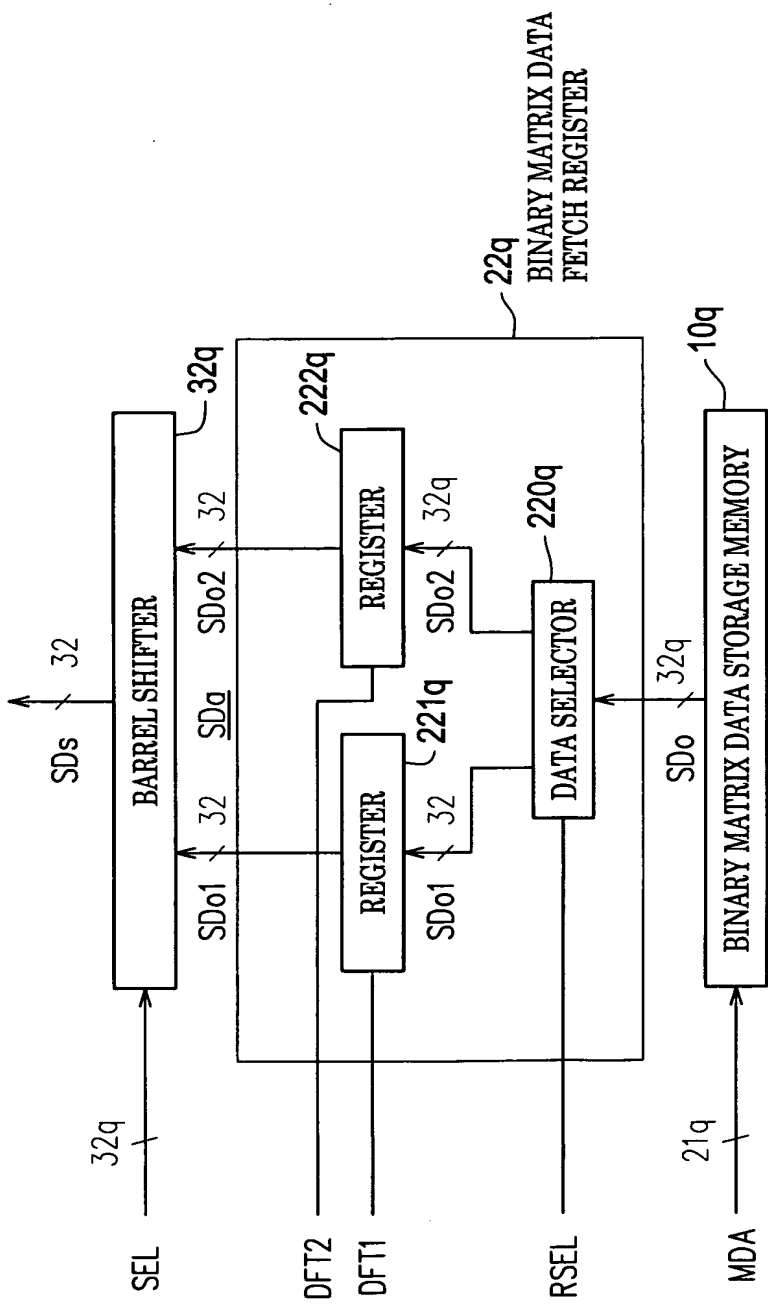


Fig. 45

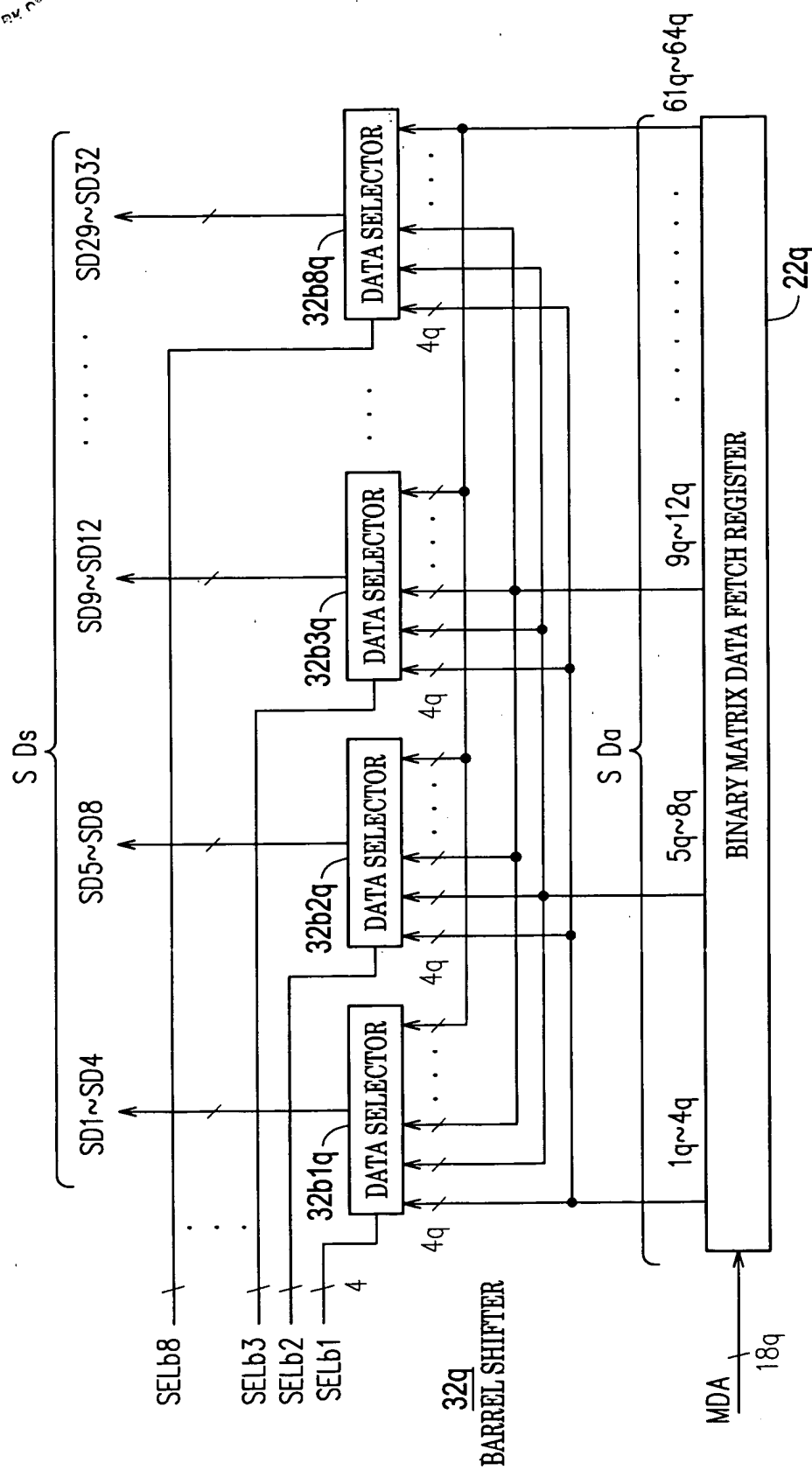
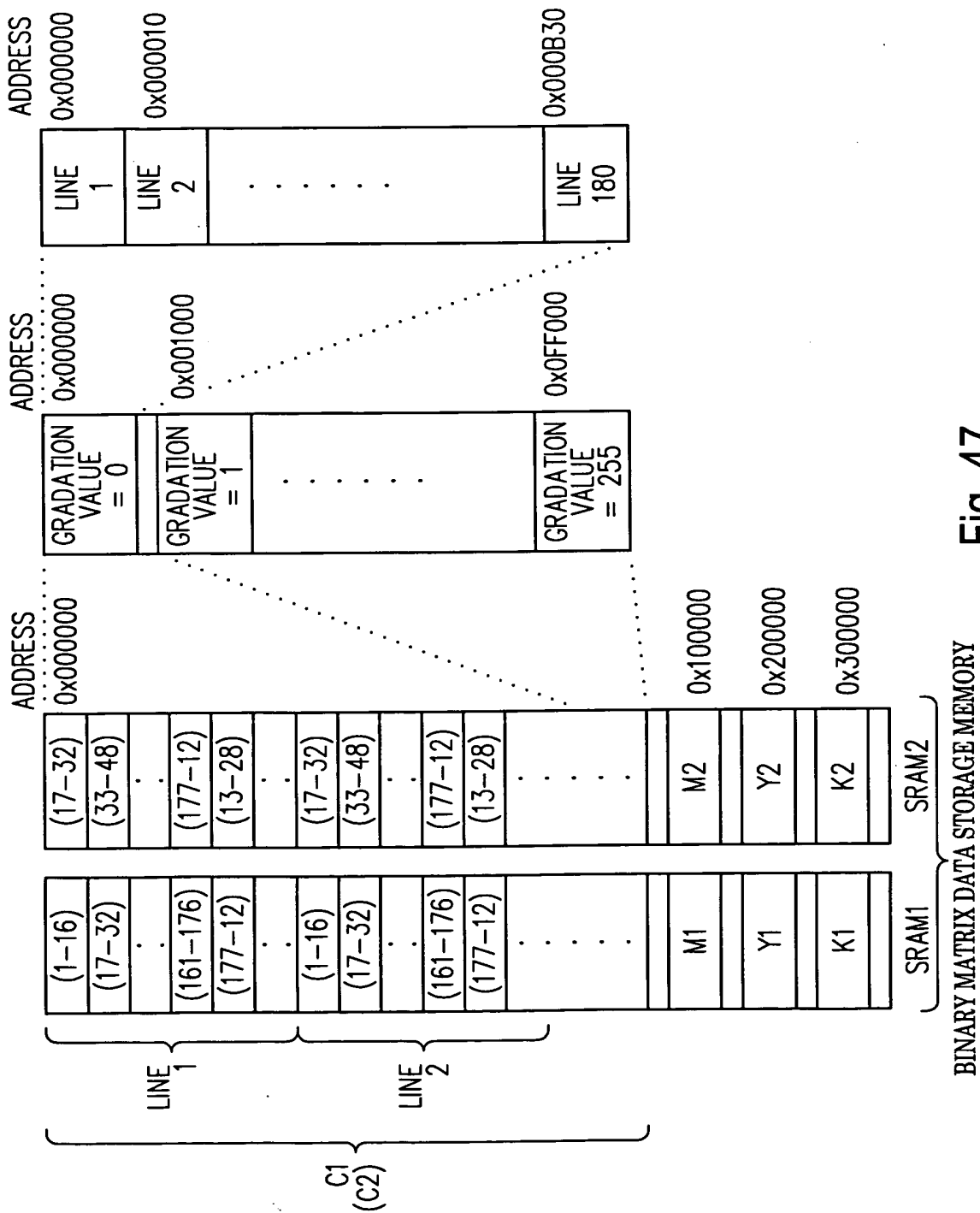
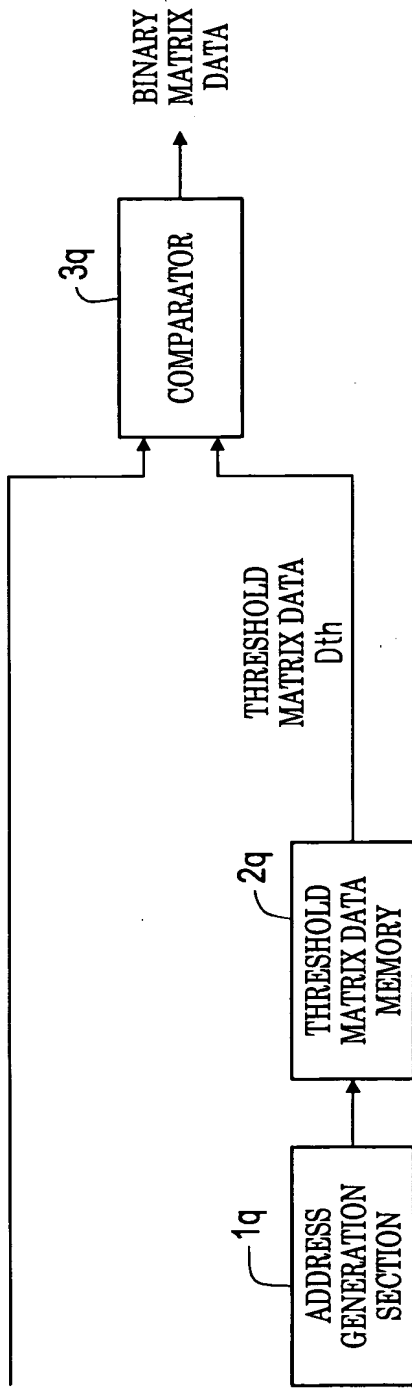


Fig. 42



INPUT
MULTI-VALUE
IMAGE DATA

D_{in}



"PRIOR ART"

(mv)

see amendment

8/25/03

Fig. 48